

PikeOS

Certifiable RTOS with Hypervisor Functionality



PikeOS for MPU

Real-Time Partitioning for MPU Architectures



INTRODUCTION

The PikeOS operating system is a light-weight virtualization system that is capable of hosting nearly all industry standard operating systems and execution environments. It supports the most stringent real-time requirements emerging from Safety-related industries, even on multi-core processor hardware.

PikeOS is available for a whole range of embedded hardware architectures, such as x86, PPC, ARM, supporting systems with or without a Memory Management Unit (MMU).

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TABLE OF CONTENTS

1.	Introduction	3
2.	Base Products	3
	- PikeOS, PikeOS for MPU	
3.	Guest Operating Systems and APIs	3
	- PikeOS Native, ARINC 653 (aka APEX), POSIX, Linux	
4.	Hardware Virtualization	3
5.	Guest OS on Demand	3
6.	Driver Framework	4
7.	Additional Components	4
	- Certifiable File System (CFS), Certifiable IP-Stack (CIP), Certifiable Math Library (CML), AFDX® Data Network End System	
8.	Certification Kits	4
	- Avionics, Automotive, Industrial Automation, Railway	
9.	Development IDE and Tools	5
	- Integrator Suite, Developer Suite	
10.	Development Host Computer Requirements	6
11.	Partner Ecosystem	6
	- Board Vendors, Silicon Vendors, Software Partners, Tool Partners	
12.	Technical Data	6

1. INTRODUCTION

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2. BASE PRODUCTS

PikeOS

PikeOS for MMU - or just PikeOS - is the classic version that is available since 2003. It started as a micro-kernel with real-time capabilities and has matured to the platform of choice in the embedded Safety and Security domain. PikeOS combines real-time and virtualization in a unique way. Technically, it is a type-1 hypervisor that executes on the bare metal.

PikeOS for MPU

Just like its bigger brother, PikeOS for MPU is an RTOS designed for highest levels of Safety & Security. It shares nearly the same native API (application, drivers), the same customer development environment (CODEO), the same SYSGO development and test environment and is being developed under the same qualification processes.

3. GUEST OPERATING SYSTEMS AND APIS

PikeOS Native

The PikeOS native API is primarily designed in order to build para-virtualized guest operating systems. It provides a small but complete set of low level system calls that are optimized for performance, robustness, Safety and Security.

The PikeOS native API is also the first choice when it comes to software to be certified at the highest certification levels.

ARINC 653 (aka APEX)

The PikeOS ARINC 653 is built upon the PikeOS native API and provides a complete and fully functional APEX API target for Avionics systems, in particular with respect to Integrated Modular Avionics (IMA). The ARINC 653 guest operating system is certifiable according to DO-178C DAL A.

POSIX

The POSIX API implements a subset of the PSE52 real-time controller system profile. It is a good choice for medium Safety-critical systems where a huge number of operating system services are required, but the use of Linux would still be too risky. It is often used for Automotive projects that require compliance to ISO 26262. The POSIX API is the backbone of the AUTOSAR adaptive solution.

Linux

When a large set of features is required, Linux is the operating system of choice. Although PikeOS has no restrictions on the Linux distribution to be hosted, the in-house ELinOS is recommended. It offers the most straight forward integration into a PikeOS virtual machine as well as dedicated extensions in order to directly use enhanced PikeOS features.

Linux operating system partitions are often used alongside a POSIX or ARINC 553 partition, setting up a an overall system with mixed criticality. The PikeOS hypervisor technology ensures that a running Linux OS has no impact on the certification aspects of an API with a higher degree of criticality. Linux operating systems can be run in hardware- as well as para-virtualized mode.

4. HARDWARE VIRTUALIZATION

The PikeOS hardware virtualization solution allows the execution of a complete guest operating system inside a partition without the need of modifying it. Hardware virtualization is available for the ARM architecture.

5. GUEST OS ON DEMAND

The following partition types are available on demand:

Guest OS	Description
Certified POSIX	on PikeOS 3.1/3.4 in certification context only. Also planned for PikeOS 5.1.x
RTEMS	available for PikeOS 3.4 on SPARC
Android	available only as customized Engineering services
AUTOSAR classic	Partner solution possible via Vector
AUTOSAR adaptive	Partner solution possible via Vector
Java	via partner Aicas "Jamaica". Requires POSIX or Linux guest OS

6. DRIVER FRAMEWORK

As a virtualization solution, PikeOS offers different realms where user device drivers can be implemented:

- As user space application with direct memory mapped access to the hardware I/O resources. The application may either directly utilize the hardware exclusively on its own or offer access to other user space applications by means of the file provider API.
- As system extension in the PikeOS System Software. This method is still available for PikeOS 5.x, but is marked as deprecated
- Within the PSP (KLDD). These are small drivers that are typically used during board bring up (e.g. serial console) and usually specific to the according PSP
- Within the PikeOS kernel (KDEV). Character-, block-drivers and volume providers are available

Standard PikeOS BSPs come at least with an Ethernet and serial driver. The whole range of PikeOS drivers comprises:

- CAN
- DIO
- I2C
- RTC
- Watchdog
- SPI
- PCI/PCIe
- QSPI
- NAND Flash
- NOR Flash
- MMC Mass Storage
- SATA
- GPU
- USB Mass Storage
- AFDX

7. ADDITIONAL COMPONENTS

Certifiable File System (CFS)

The Certifiable File System (CFS) is a PikeOS component that provides a fail safe file system with more functionality than the PikeOS native file system. In addition to the basic file operations implemented by the internal PikeOS file providers, the CFS can also handle directories and file manipulations. It is still a simplified file system compared

to standard Linux file systems and is certifiable within the scope of Safety projects.

Certifiable IP-Stack (CIP)

CIP is a UDP/IP networking stack compatible with the standard RFC specifications. It is available for the POSIX and PikeOS APIs and provides a standard socket interface. CIP is certifiable within the scope of Safety projects.

Certifiable Math Library (CML)

The CML provides various mathematical functions of the C standard library. CML is certifiable within the scope of Safety projects.

AFDX® Data Network End System

"Avionic Full-Duplex Switched Ethernet" (ARINC 664 Part 7) is a deterministic aircraft data network bus system for Avionics systems. The network is based on standard IEEE802.3 Ethernet technology. SYSGO's implementation is software based and runs on COTS hardware.

PikeOS drivers are available. All artefacts required to process a DO-178C certification are available. The currently available documents will cover a certification up to Level A. The software stack can also be used stand-alone out of the context of an operating system, e.g. on switches.

7. CERTIFICATION KITS

PikeOS Certification Kits (CertKit) provide all necessary artefacts to prove the compliance of PikeOS to all objectives of the Safety and Security standards. By using the PikeOS CertKit, SYSGO customers can focus on the certification of their application(s).

The following Certification Kits are available:

Market	Standard	Assured Safety Level	Explanation
Automotive	ISO 26262	ASIL A – ASIL D	Automotive Safety Integrity Level
Avionics	DO-178C	DAL E – DAL A	Development Assurance Level
Functional Safety (General, Industrial Automation)	IEC 61508	SIL 1 – SIL 3	Safety Integrity Level
Railway Signalling & Rolling Stock	EN 50128, EN 50657	SIL 1 – SIL 4	Safety Integrity Level

Common Criteria: Security certification according to Common Criteria EAL 5+ (PikeOS 5.1.3)

8. DEVELOPMENT IDE AND TOOLS

The Eclipse-based IDE CODEO supports system architects with graphical configuration tools, provides all the components software engineers need to develop embedded applications and includes comprehensive little helpers to finish embedded projects in a time-saving and cost-efficient way: Guided configuration, remote debugging (down to the hardware instruction level), target monitoring, remote application deployment, and timing analyses. Of course, CODEO provides standard application development features such as compiler, assembler and linker.

- C/C++ Code Editor
- Syntax Highlighting
- Code Completion
- Source Navigation
- Type Hierarchy
- Call Graph
- Include Browser
- Macro Definition Browser
- Visual Debugging Tools, Views displaying
 - Memory
 - Registers
 - Disassembly

The PikeOS workflow defines two different working places, the Integrator Suite and the Application Suite.

Integrator Suite

The Integrator Suite comprises tools that are required to setup and manage virtual machines (aka resource partitions). Amongst others, these are:

- **Graphical Editor for the Virtual Machine Initialization Table (VMIT):** Allows to statically define the resource partitions for a PikeOS system. The definition comprise I/O and memory resources as well as processor affinity and processor time constraints. Inter-partition communication can be established by means of shared memory, drivers, ports and channels. The VMIT editor provides a graphical representation for each of those entities and allows to configure the system in a very detailed way.
- **Graphical Editor for the ROM-Image Builder:** While the VMIT Editor configures the statically assigned resources, the ROM image builder configures the system- and user-files that are present on the running PikeOS system. In addition to services and daemons, the system-files typically consist of the PikeOS kernel and the PikeOS System Software. Both are available in different versions, such as certified and non-certified, debug and non-debug versions. User files typically consists of executables and data files. Except for system limits in terms of storage memory there is no constraint on what the user may add to the system. The ROM-Image Builder also allows to configure user-properties that can be read at runtime and provides the developer to hierarchically build up a tree of system settings.

- **Project Editor:** Both the VMIT-Editor and the ROM-Image Builder are very enhanced tools and allow to setup a system in a very distinct way. In addition, there are cross reference and dependencies between both them. Therefore the project editor provides a top level view on the system and adjusts the the major items in the VMIT- and ROM-Image Builder accordingly. E.g. the project editor assumes that an application running in a partition requires an executable file and automatically provides a placeholder in the ROM-Image builder. It also creates all cross-references and access rights automatically. In case the integrator is not contended with the settings, the details can be changes in the two low-level tools. The project editor recognizes the changes and would not override them. However, the integrator is warned in case of inconsistencies or dangling ends.

The Integrator Suite also contains user-guided wizards for a quick project-start as well as validation tools for certification issues. The VMIT- as well as the ROM-Image builder are validated tools with regards to a DO-178C certification.

Developer Suite

The developer Suite comprises all tools that are required for application development. In general, two tool-chains are available:

- **GCC C/C++ based cross compiler:** Applicable to all kinds to application types, certifiable up to DO-178C DAL B.
- **AdaCore GNAT Pro C/C++ compiler:** Applicable for certification projects that a require a design assurance level of DAL A. In addition to C/C++, the Ada language is available for PikeOS native applications and ARINC 653 partition types.

Each guest operating system and API (see above) comes with its own developer suite. The tool-chain is fully integrated into the development IDE and the necessary compiler switches, header files and libraries are managed automatically based on the according guest operating system. The developer suite also provides these tools:

- Graphical application setup wizards
- **Project Editor:** Allows to switch between debugging/ executable mode. Also allows to configure additional components and libraries. The project editor also manages the required compiler settings.
- Graphical debugger
- Static analysis tools
- Dynamic analysis tools (system-, application- and user-defined tracing)
- **Muxa:** Allows to multiplex communication methods (e.g. Ethernet, serial) between multiple start- and end-points. This is especially useful in case you are debugging multiple partitions with multiple consoles and debugging ports at the same time.

9. DEVELOPMENT HOST COMPUTER REQUIREMENTS

- 64-bit Linux distributions
- Windows 10 (64-bit)

10. SUPPORT

Standard support for the first year is included in standard product pricing. It contains analysis of reproducible errors in and malfunctioning of software developed by SYSGO and provision of known error corrections, as well as support in preparing work-around solutions.

Optional "Premium Support" offers additionally direct access to a dedicated support engineer and limited hours of consulting. "Long Term Support" offers a retaining ability to rebuild the selected frozen version, a limited number of consulting hours, a dedicated phone number and access to a wide data base of corrections, updates, demo programs and others.

Certified product versions profit from "Product Cert Support" and "Long-Term Cert Support" that includes Safety and Security bulletins that inform the customer of vulnerabilities or Safety risks. Customer support is reserved to customers owning a valid support contract.

For more information, please get in contact with SYSGO Sales team at www.sysgo.com/contact

11. PARTNER ECOSYSTEM

SYSGO is committed to establish the technological and business partnerships that will help its customers to achieve their goals. SYSGO is currently working with about 100 partners worldwide. A list of available partners that help to enhance the value, can be found here: www.sysgo.com/partners

An excerpt of partners per category is mentioned below:

Board Vendors

ADVANTECH, Curtiss-Wright Controls Embedded Computing, Kontron, PHYTEC, cetrac, ecrin, ELTEC, EMCOMO, EMERSON, gaci, mercury systems, TQ systems, TTEch, X-ES or ABACO

Silicon Vendors

AMD, arm, ATMEL, AMCC, ARROW, NXP, Renesas, TI, Xilinx, Infineon, Nvidia, MIPS, Teledyne e2v, STMicroelectronics or Intel

Software Partners

CoreAVI, Aicas, AdaCore, ANSYS, CANDERA, Datalight, DISTI, FLEXERA, Guard KNOX, RTI, PrismTech, Datalight, IS2T, iSYSTEM, Karamba Security, Médiane Système, Object Interface Systems, Quantum 3D, Systerel, Imagination Technologies, PRESAGIS, PrismTech, Rightware, Swell Software, TUXERA , wolfSSL or RAPITA

Tool Partners

AbsInt , LDRA, Lauterbach, Vector Software, ZODIAC AEROSPACE, PEAK System, SYMTA VISION, Solid Sands, Adacore, Rapita, ARM, ANSYS

12. TECHNICAL DATA

Description	Value
Maximum Number of Resource Partitions	256
Maximum Number of Time Partitions	256
Maximum Number of Tasks	1023
Maximum Number of Threads	4096
Maximum Number of CPUs	32 – 64, based on processor architecture

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