

INTERNATIONAL TELECOMMUNICATION UNION

**TELECOMMUNICATION  
STANDARDIZATION SECTOR**

STUDY PERIOD 2013-2016

**STUDY GROUP 15  
TD 677 (PLEN/15)**

**English only**

**Original: English**

---

**Question(s):** 10/15

Geneva, 19-30 September 2016

**TD**

**Source:** Editor G.8121/Y.1381

**Title:** Draft Corrigendum 1 to Recommendation G.8121/Y.1381 (2016) (for Consent, 30 September 2016)

---

**Abstract**

This document provides Draft Corrigendum 1 to G.8121 (2016) per the agreement on C.1928.

---

**Contact:** Yuji Tochio  
Fujitsu  
Japan

Tel: +81-44-754-8829  
Email: [tochio@jp.fujitsu.com](mailto:tochio@jp.fujitsu.com)

**Attention:** This is not a publication made available to the public, but **an internal ITU-T Document** intended only for use by the Member States of ITU, by ITU-T Sector Members and Associates, and their respective staff and collaborators in their ITU related work. It shall not be made available to, and used by, any other persons or entities without the prior written consent of ITU-T.

## **Annex**

### **Corrigendum 1 to Recommendation ITU-T G.8121/Y.1381**

#### **Characteristics of MPLS-TP equipment functional blocks: Corrigendum 1**

##### **Summary**

Corrigendum 1 to Recommendation ITU-T G.8121/Y.1381 (2016) removes irrelevant indexes in a few “OAM Tool” MIs.

## Corrigendum 1 to Recommendation ITU-T G.8121/Y.1381

### Characteristics of MPLS-TP equipment functional blocks: Corrigendum 1

#### 1) Scope of Corrigendum 1

This corrigendum removes irrelevant indexes in a few “OAM Tool” MIs.

#### 2) Clause 9.2.1.1, MPLS-TP Trail Termination Source function (MT\_TT\_So)

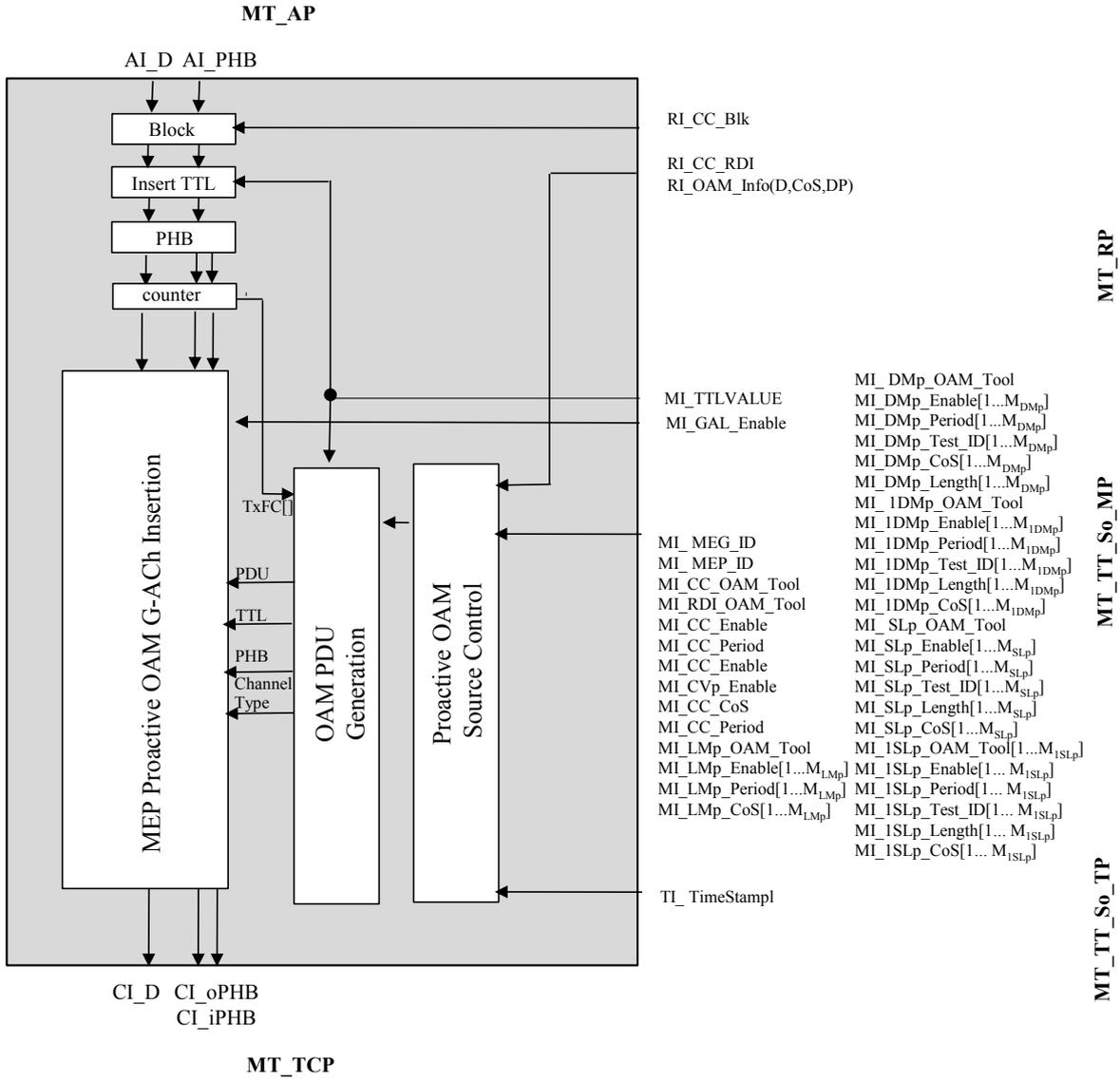
Update Table 9-2 as indicated:

Table 9-2/G.8121/Y.1381 – MT\_TT\_So inputs and outputs

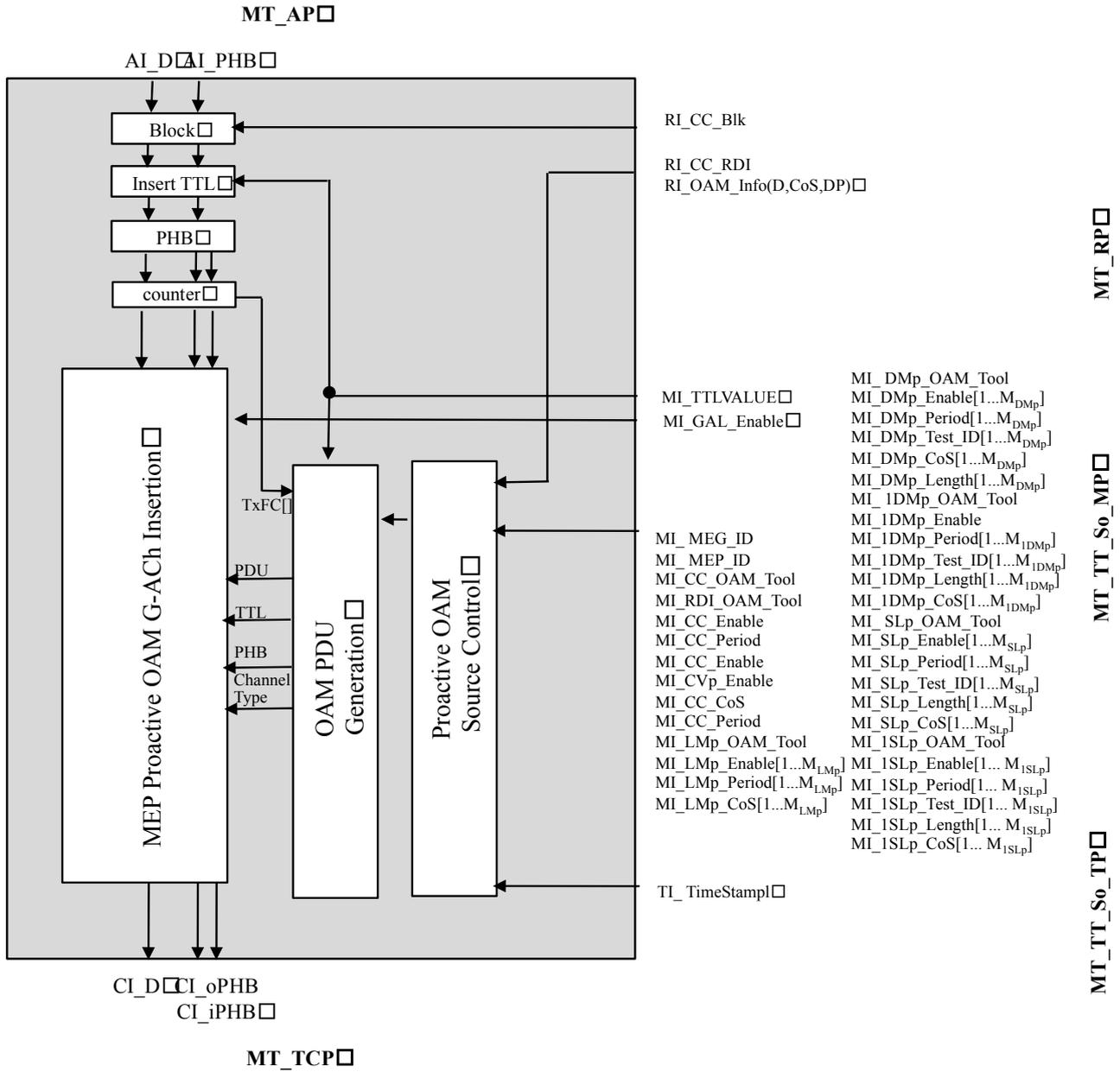
Input(s)	Output(s)
<b>MT_AP:</b> MT_AI_D MT_AI_PHB  <b>MT_RP:</b> MT_RI_CC_RDI MT_RI_CC_BlK MT_RI_OAM_Info(D,CoS,DP)  <b>MT_TT_So_MP:</b> MT_TT_So_MI_GAL_Enable MT_TT_So_MI_TTLVALUE MT_TT_So_MI_MEG_ID MT_TT_So_MI_MEP_ID  MT_TT_So_MI_CC_OAM_Tool MT_TT_So_MI_RDI_OAM_Tool MT_TT_So_MI_CC_Enable (Note) MT_TT_So_MI_CVp_Enable (Note) MT_TT_So_MI_CC_CoS MT_TT_So_MI_CC_Period  MT_TT_So_MI_ILMp_Enable  MT_TT_So_MI_LMp_OAM_Tool[1...M <sub>LMp</sub> ] MT_TT_So_MI_LMp_Enable[1...M <sub>LMp</sub> ] MT_TT_So_MI_LMp_Period[1...M <sub>LMp</sub> ] MT_TT_So_MI_LMp_CoS[1...M <sub>LMp</sub> ]  MT_TT_So_MI_DMp_OAM_Tool[1...M <sub>DMp</sub> ] MT_TT_So_MI_DMp_Enable[1...M <sub>DMp</sub> ] MT_TT_So_MI_DMp_Period[1...M <sub>DMp</sub> ] MT_TT_So_MI_DMp_Test_ID[1...M <sub>DMp</sub> ]	<b>MT_TCP:</b> MT_CI_D MT_CI_oPHB MT_CI_iPHB  <b>MT_RP:</b>

<p>MT_TT_So_MI_DMp_CoS[1...M<sub>DMp</sub>] MT_TT_So_MI_DMp_Length[1...M<sub>DMp</sub>]</p> <p>MT_TT_So_MI_1DMp_OAM_Tool[1...M<sub>1DMp</sub>] MT_TT_So_MI_1DMp_Enable[1...M<sub>1DMp</sub>] MT_TT_So_MI_1DMp_Period[1...M<sub>1DMp</sub>] MT_TT_So_MI_1DMp_Test_ID[1...M<sub>1DMp</sub>] MT_TT_So_MI_1DMp_Length[1...M<sub>1DMp</sub>] MT_TT_So_MI_1DMp_CoS[1...M<sub>1DMp</sub>]</p> <p>MT_TT_So_MI_SLp_OAM_Tool[1...M<sub>SLp</sub>] MT_TT_So_MI_SLp_Enable[1...M<sub>SLp</sub>] MT_TT_So_MI_SLp_Period[1...M<sub>SLp</sub>] MT_TT_So_MI_SLp_Test_ID[1...M<sub>SLp</sub>] MT_TT_So_MI_SLp_Length[1...M<sub>SLp</sub>] MT_TT_So_MI_SLp_CoS[1...M<sub>SLp</sub>]</p> <p>MT_TT_So_MI_1SLp_OAM_Tool[1...M<sub>1SLp</sub>] MT_TT_So_MI_1SLp_Enable[1...M<sub>1SLp</sub>] MT_TT_So_MI_1SLp_Period[1...M<sub>1SLp</sub>] MT_TT_So_MI_1SLp_Test_ID[1...M<sub>1SLp</sub>] MT_TT_So_MI_1SLp_Length[1...M<sub>1SLp</sub>] MT_TT_So_MI_1SLp_CoS[1...M<sub>1SLp</sub>]</p> <p><b>MT_TP:</b> MT_TT_So_TI_TimeStamp</p>	
<p>NOTE- MI_CC_Enable and MI_CVp_Enable are used to enable CC and CV functions respectively. The possible combinations are:</p> <ul style="list-style-type: none"> <li>- no CC function and no CV function: MI_CC_Enable = false and MI_CVp_Enable = false</li> <li>- CC-only function: MI_CC_Enable = true and MI_CVp_Enable = false</li> <li>- CC and CV functions: MI_CC_Enable = true and MI_CVp_Enable = true</li> </ul>	

Replace Figure 9-5:



By:



**3) Clause 9.2.1.2, MPLS-TP Trail Termination Sink function (MT\_TT\_Sk)**

Update Table 9-3 as indicated:

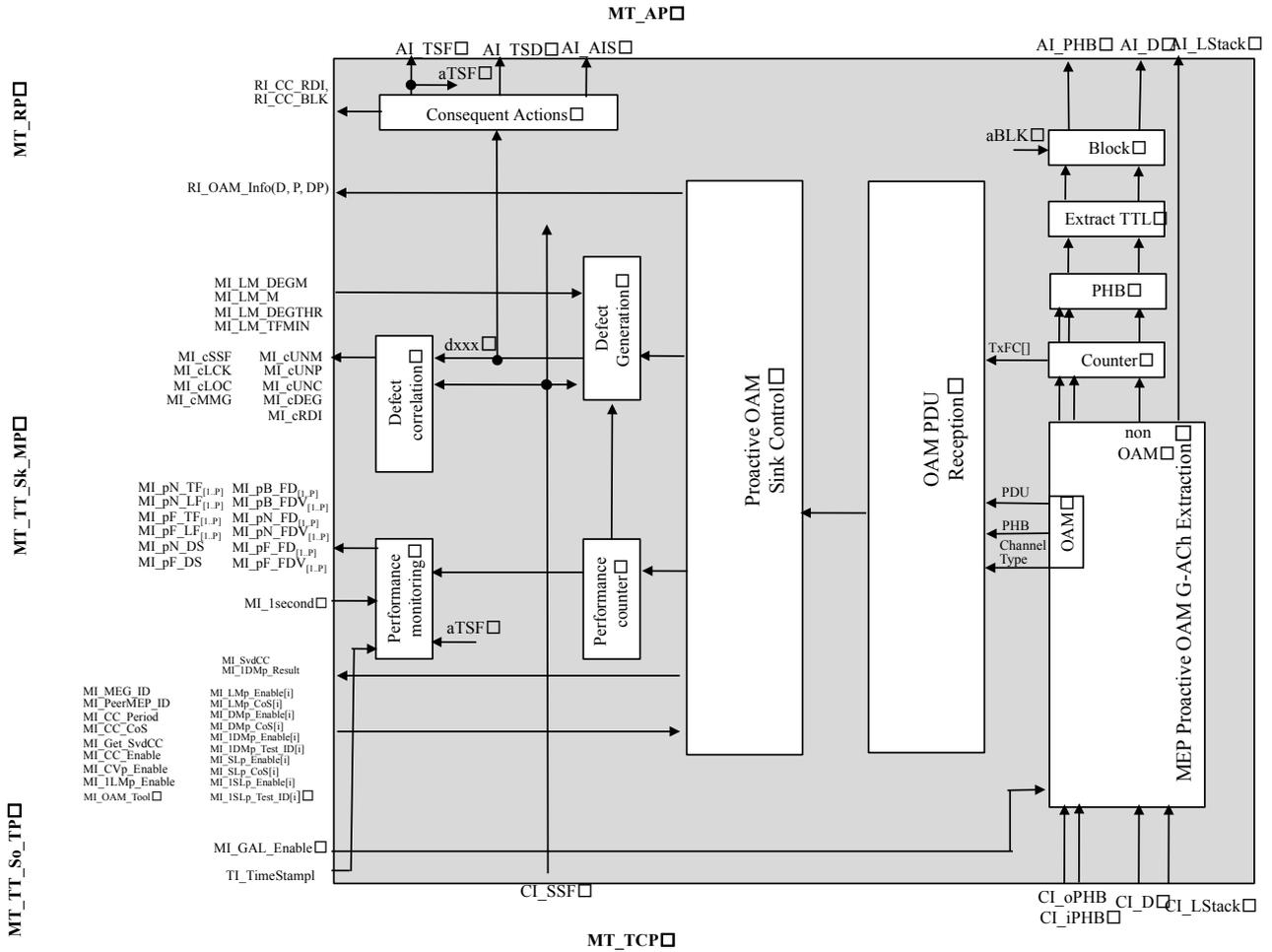
**Table 9-3/G.8121/Y.1381 – MT\_TT\_Sk inputs and outputs**

Input(s)	Output(s)
<p><b>MT_TCP:</b></p> <p>MT_CI_D</p> <p>MT_CI_iPHB</p> <p>MT_CI_oPHB</p> <p>MT_CI_SSF</p> <p>MT_CI_Lstack</p>	<p><b>MT_AP:</b></p> <p>MT_AI_D</p> <p>MT_AI_PHB</p> <p>MT_AI_TSF</p> <p>MT_AI_TSD</p> <p>MT_AI_AIS</p>

Input(s)	Output(s)
<p><b>MT_RP:</b></p> <p><b>MT_TT_Sk_MP:</b>            MT_TT_Sk_MI_GAL_Enable            MT_TT_Sk_MI_MEG_ID            MT_TT_Sk_MI_PeerMEP_ID            MT_TT_Sk_MI_CC_OAM_Tool            MT_TT_Sk_MI_RDI_OAM_Tool</p> <p>MT_TT_Sk_MI_CC_Enable(Note)            MT_TT_Sk_MI_CVp_Enable (Note)</p> <p>MT_TT_Sk_MI_CC_Period            MT_TT_Sk_MI_CC_CoSMT_TT_Sk_MI_Get_SvdCC</p> <p>MT_TT_Sk_MI_1LMp_Enable</p> <p>MT_TT_Sk_MI_LMp_OAM_Tool[1...M<sub>LMp</sub>]            MT_TT_Sk_MI_LMp_Enable[1... M<sub>LMp</sub>]            MT_TT_Sk_MI_LMp_CoS[1... M<sub>LMp</sub>]            MT_TT_Sk_MI_LM_DEGM            MT_TT_Sk_MI_LM_M            MT_TT_Sk_MI_LM_DEGTHR            MT_TT_Sk_MI_LM_TFMIN</p> <p>MT_TT_Sk_MI_DMp_OAM_Tool[1...  <del>M<sub>DMp</sub></del>]            MT_TT_Sk_MI_DMp_Enable[1... M<sub>DMp</sub>]            MT_TT_Sk_MI_DMp_CoS[1... M<sub>DMp</sub>]</p> <p>MT_TT_Sk_MI_1DMp_OAM_Tool  <del>[1...M<sub>1DMp</sub>]</del>            MT_TT_Sk_MI_1DMp_Enable[1...M<sub>1DMp</sub>]            MT_TT_Sk_MI_1DMp_Test_ID[1...M<sub>1DMp</sub>]</p> <p>MT_TT_Sk_MI_SLp_OAM_Tool[1...<del>M<sub>SLp</sub></del>]            MT_TT_Sk_MI_SLp_Enable[1... M<sub>SLp</sub>]            MT_TT_Sk_MI_SLp_CoS[1... M<sub>SLp</sub>]</p> <p>MT_TT_Sk_MI_1SLp_OAM_Tool            MT_TT_Sk_MI_1SLp_Enable[1...M<sub>1SLp</sub>]            MT_TT_Sk_MI_1SLp_Test_ID[1...M<sub>1SLp</sub>]</p> <p>MT_TT_Sk_MI_AIS_OAM_Tool            MT_TT_Sk_MI_LCK_OAM_Tool</p> <p>MT_TT_Sk_MI_1second</p>	<p>MT_AI_LStack</p> <p><b>MT_RP:</b>            MT_RI_CC_RDI            MT_RI_CC_BlK</p> <p>MT_RI_OAM_Info(D,CoS,DP)</p> <p><b>MT_TT_Sk_MP:</b>            MT_TT_Sk_MI_SvdCC            MT_TT_Sk_MI_cSSF            MT_TT_Sk_MI_cLCK            MT_TT_Sk_MI_cLOC            MT_TT_Sk_MI_cMMG            MT_TT_Sk_MI_cUNM            MT_TT_Sk_MI_cUNP</p> <p>MT_TT_Sk_MI_cUNC</p> <p>MT_TT_Sk_MI_cDEG            MT_TT_Sk_MI_cRDI            MT_TT_Sk_MI_pN_LF[1...P]            MT_TT_Sk_MI_pN_TF[1...P]            MT_TT_Sk_MI_pF_LF[1...P]            MT_TT_Sk_MI_pF_TF[1...P]            MT_TT_Sk_MI_pF_DS            MT_TT_Sk_MI_pN_DS            MT_TT_Sk_MI_pB_FD[1...P]            MT_TT_Sk_MI_pB_FDV[1...P]            MT_TT_Sk_MI_pN_FD[1...P]            MT_TT_Sk_MI_pN_FDV[1...P]            MT_TT_Sk_MI_pF_FD[1...P]            MT_TT_Sk_MI_pF_FDV[1...P]</p>

Input(s)	Output(s)
<b>MT_TP:</b> MT_TT_Sk_TI_TimeStamp	
NOTE - See NOTE in Table 9-2	

Replace Figure 9-7:



By:

