

# **Editor's Report 60802 Draft 2.0**

## **January 2023**

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# Expected contributions

Description	Contributor(s)	Status	Remarks
A contribution clarifying the depiction of the transmit and receive paths and the positioning of middleware in figures 2 & 3	Guenter/Marius/Mark	Received	Incorporated in d2.0
A contribution describing the queueing of frames on ingress (figure 3). An alternative to the term "queueing frames" will be considered as the term may be confused with 802.1Q, 8.6.6.	Guenter/Marius/Mark	Received	Incorporated in d2.0. Term queueing frames eliminated in figure 3
A contribution breaking the 8 capabilities defined in RFC 6241 into atomic requirements and options	Martin	Received	Incorporated in d2.0
Contribution specifying the exact gPTP parameters and associated values in tables 9 and 10	Time Sync adhoc	Received	Parameters for control loop and global clock are missing
Contribution specifying total noise budget, including Dynamic timestamp error	Time Sync adhoc	Received	Parameters for control loop and global clock are missing
A contribution outlining the specific requirements for Grandmaster PTP Instances	Time Sync adhoc	Received	Parameters for control loop and global clock are missing
Update figure 13 to include clock state information.	Guenter	Received	Incorporated in d2.0
A contribution clarifying figures 17 and 18 and improving the readability of both figures	Guenter	Received	Incorporated in d2.0
Textual contribution covering the resolution of D1.4 comments against security (not covered by other contributions identified herein)	Oliver/Andreas/Kai	Received	Incorporated in d2.0
Textual contribution on the Secure Device Identity Profile	Oliver/Andreas/Kai	Waiting for feedback from the discussion with NIST (conducted by the IEEE 802.1 Security Taskgroup)	Encompasses: - Detailing the use of IEEE 802.1AR esp. clarifying the supported elements of IEEE Std 802.1AR - Addressing the use of RSA-2048/SHA-256 and in addition, the use of RSA with other key lengths - Multiple IDevID credentials per IA station to support multiple cryptographic algorithms
Textual contribution on Resource Access Authorization	Oliver/Andreas/Kai	Open (expect initial proposal for discussion [form factor: pptx] during Q1/2023)	Encompasses: - Expanding access control to all YANG modules esp. elaborating their authorization model and associated roles - Expanding the role names and including vendor-specific and owner-operator roles
A contribution regarding the specific YANG features and leaves required for security	Oliver/Andreas/Kai	Open	Considers the security-specific YANG modules (ietf-truststore, ietf-keystore, ietf-crypto-types, ietf-x509-cert-to-name, ietf-netconf-acm)
Textual contribution regarding Internal key generation (see comment 468)	Oliver/Andreas/Kai	Open	
IA-station Figures Update	Stephan/Guenter/Mark	Received	Incorporated in d2.0
Contribution regarding VLAN naming scheme	Josef	Received	Incorporated in d2.0
Contribution regarding the form of the digital data sheet	Stephan/Guenter/Mark	Open	
Source Diagrams for figures 26 and 27	Nemanja	Received	Not yet incorporated in d2.0
Contribution revising Annex Z	Mark	Open	
Contribution regarding proper specification of PHY	In collaboration w/ 802.3	Received	Incorporated in d2.0

# Status

- All but 1 of the received contributions incorporated
- All comments reviewed
  - 434 Implemented
  - 27 comments are dependent on outstanding contributions
  - 1 comment (63) requires further discussion

**Thank you**