

IEC / IEEE 60802 - IA profile

IEEE802.1Q identified gaps –

Rules for gates control vs. inter packet gap

-To be discussed-

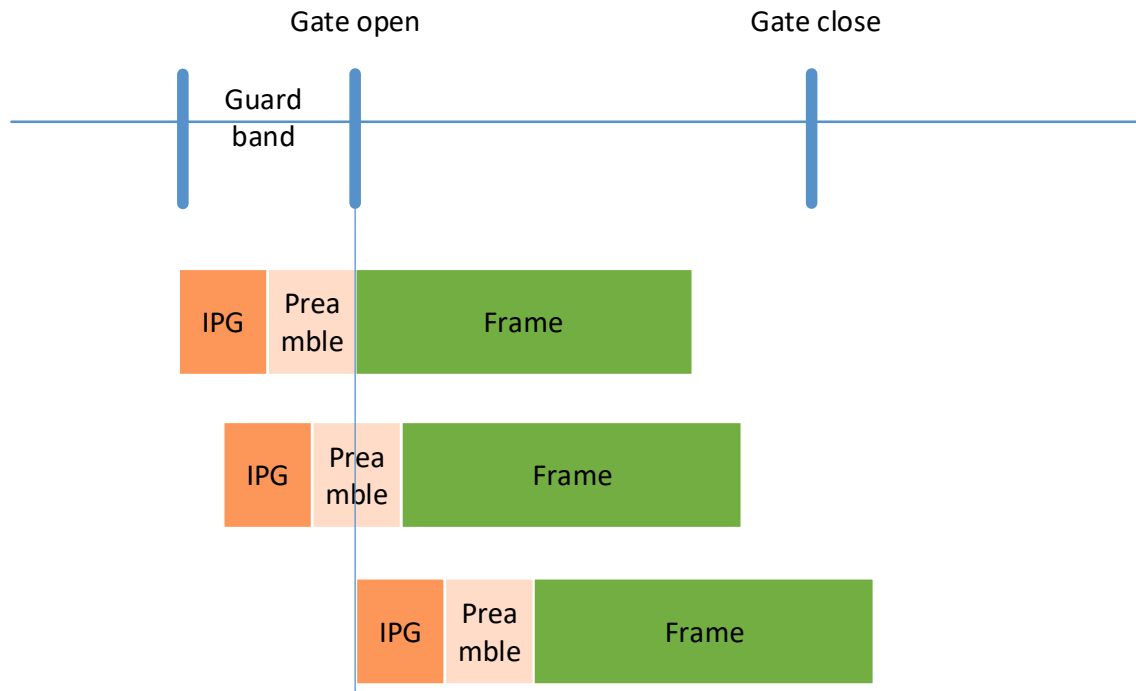
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Basic scope

IEEE 802.1Q introduces time aware shaper and thus gate control.

Calculating the bandwidth available inside / outside a gated network access needs clear statements.

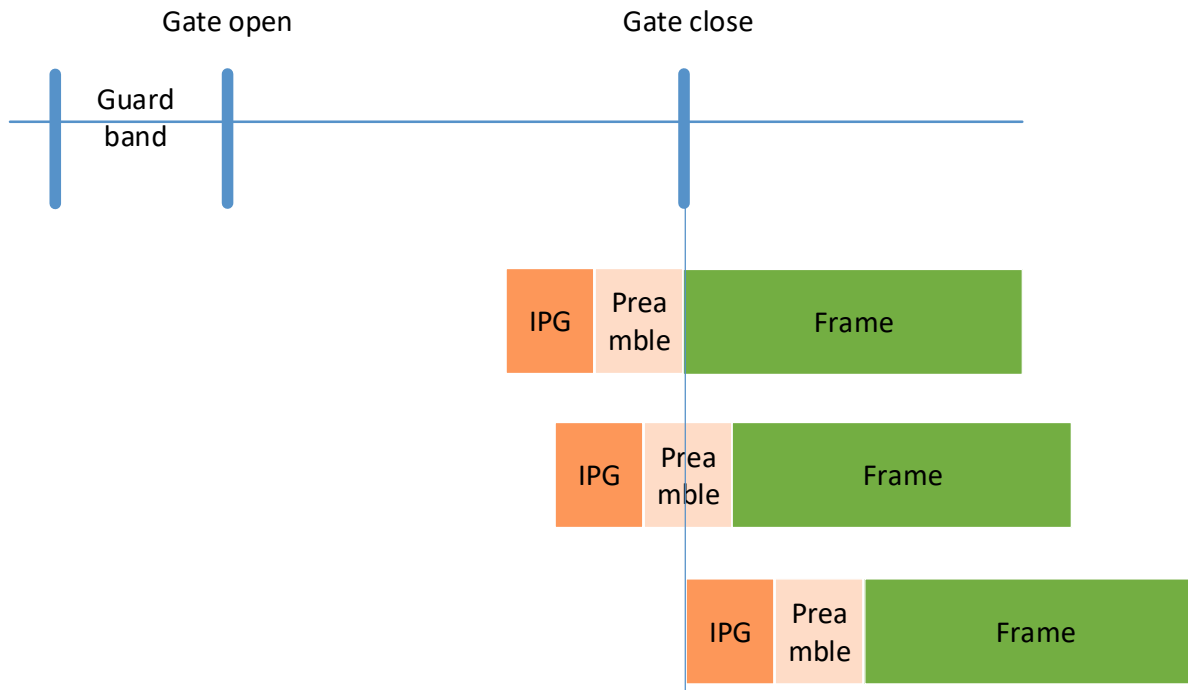
Gate open - Inter packet gap is where?



What is the rule?

Outside, inside or undefined?

Gate close - Inter packet gap is where?



What is the rule?

Outside, inside or undefined?

Conclusion

A clear definition for both gates is needed.

If not already stated inside IEEE802.1Q, a definition is needed.

Thank you

Questions?