# Masked Computation of the Floor Function and Its Application to the FALCON Signature 

Justine Paillet ${ }^{1,3}$ [0009-0009-6056-7766] , Pierre-Augustin<br>Berthet ${ }^{2,3[0009-0005-5065-2730]}$, and Cédric Tavernier ${ }^{3[0009-0007-5224-492 X]}$<br>${ }^{1}$ Université Jean-Monnet, Saint-Étienne, France, justine.paillet@univ-st-etienne.fr<br>${ }^{2}$ Télécom Paris, Palaiseau, France, berthet@telecom-paris.fr<br>${ }^{3}$ Hensoldt SAS FRANCE, Plaisir, France,<br><pierre-augustin.berthet, justine.paillet, cedric.tavernier>@hensoldt.net


#### Abstract

FALCON is candidate for standardization of the new Post Quantum Cryptography (PQC) primitives by the National Institute of Standards and Technology (NIST). However, it remains a challenge to define efficient countermeasures against side-channel attacks (SCA) for this algorithm. FALCON is a lattice-based signature that relies on rational numbers which is unusual in the cryptography field. While recent work proposed a solution to mask the addition and the multiplication, some roadblocks remain, most noticeably how to protect the floor function. We propose in this work to complete the existing first trials of hardening FALCON against SCA. We perform the mathematical proofs of our methods as well as formal security proof in the probing model using the Non-Interference concepts.


Keywords: Floor Function • Floating-Point Arithmetic • Post-Quantum Cryptography • FALCON • Side-Channel Analysis • Masking

## 1 Introduction

With the rise of quantum computing, mathematical problems which were hard to solve with current technologies will be easier to breach. Among the concerned problems, the Discrete Logarithm Problem (DLP) could be solved in polynomial times by the Shor quantum algorithm [28]. As much of the current asymmetric primitives rely on this problem and will be compromised, new cryptographic primitives are studied. The National Institute of Standards and Technology (NIST) launched a post-quantum standardization process [7]. The finalists are CRYSTALS Kyber [5,22], CRYSTALS Dilithium [9,21], SPHINCS+ [3,23] and FALCON [25].
Another concern for the security of cryptographic primitives is their robustness to a Side-Channel opponent. Side-Channel Analysis (SCA) was first introduced by Paul Kocher [18] in the mid-1990. This new branch of cryptanalysis focuses on studying the impact of a cryptosystem on its surroundings. As computations take time and energy, an opponent able to access the variation of one or both could
find correlations between its physical observations and the data manipulated, thus resulting in a leakage and a security breach. Thus, the study of weaknesses in the implementations of new primitives and the way to protect them is an active field of research.

While many works have been done on CRYSTALS Dilithium and CRYSTALS Kyber, summed up by Ravi et al. [26], FALCON has been less covered. Indeed, the algorithm relies on floating-point arithmetic, for which there is little literature on how to protect it.

Related Work Previous works have identified two main weaknesses within the signing process of Falcon: the pre-image computation and the Gaussian sampler. The latest is proved vulnerable by Karabulut and Aysu [17] using an ElectroMagnetic (EM) attack. Their work was later improved by Guerreau et al. [13]. To counter those attacks, Chen and Chen [6] propose a masked implementation of the addition and multiplication of FALCON. However, they did not delve into the second weakness of Falcon, the Gaussian sampler.
The Gaussian sampler is vulnerable to timing attacks, as shown by previous work [12,10,20,24]. A isochronous design was proposed by Howe et al. [14] to counter those attacks. Nonetheless, a successful single power analysis (SPA) was proposed by Guerreau et al. [13] and further improved by Zhang et al. [29]. There is currently no masking countermeasure for FALCON's Gaussian Sampler. Existing work [11] tends to rewrite the Gaussian Sampler to remove the use of floating arithmetic, thus avoiding the challenge of masking the floor function.

Our Contribution In this work, we further expand the countermeasure from Chen and Chen [6] and apply it to the Gaussian Sampler. We propose a masking method based on the mantissa truncation to compute the floor function as well as a method to mask the division. We discuss the application of those methods to masking FALCON.

Relying on the previous work of Chen and Chen [6], we also verify the higherorder security of our method in the probing model. Our formal proofs rely on the Non-Interference (NI) security model first introduced by Barthe et al. [1].

We provide some performances of our methods and compare them with the reference unmasked implementation and the previous work of Chen and Chen [6]. The implementation is tested on a personal computer with an Intel-Core $i 7-11800 \mathrm{H} \mathrm{CPU}$ and is not optimized.

## 2 Notation and Background

### 2.1 Notation

- We denote by $A \sim B$ the set $A$ excluding the values of set $B$, id est $(A \backsim$ $B) \bigcap B=\emptyset$. We denote by $\mathbb{K}^{-}$the negative values of the set $\mathbb{K}$ and by $\mathbb{K}^{*}$ its non-zero values.
- For $x \in \mathbb{R}$, we denote the floor function of $x$ by $\lfloor x\rfloor$.
- We will use the dot . as the separator between the integer part $i$ and the fractional part $f$ of a real number $x=i . f$.
- If $\left(b_{i}\right)$ is a 1 -bit Boolean shares for value b , we denote $\left(-b_{i}\right)$ as the 64 bit Boolean shares for $2^{64}-b$. It means that if $b=0,\left(-b_{i}\right)$ is a 64bit boolean shares for 0 , and $b=1,\left(-b_{i}\right)$ is a 64 -bit boolean shares for 0xFFFFFFFFFFFFFFFFF.

For algorithmic extracts of FALCON [25], refer to the original paper notations.

### 2.2 Diagram Legend

The diagrams in Section 5 use the same legend:

- Probing sets are denoted by $P_{i}$ or $O$ and are colored in red.
- Simulation sets are denoted by $S_{i}^{j}$ and are colored in blue.
- $t$-SNI gadgets are colored in green.
- $t-N I$ gadgets are colored in black.


### 2.3 FALCON Sign

FALCON [25] is a Lattice-Based signature using the GPV framework over the NTRU problem. In this paper, we will focus on the Gaussian Sampler used in the signature algorithm. For more details on the key generation or the verification, refer to the original paper of FALCON[25].

Signature The signature follows the Hash-Then-Sign strategy. The message $m$ is salted with a random value $r$ and then hashed into a challenge $c$. The remainder of the signature aims at building an instance of the SIS problem upon $c$ and a public key $h$, id est finding $s=\left(s_{1}, s_{2}\right)$ such as $s_{1}+s_{2} h=c$. To do so, the need to compute $\boldsymbol{s}=(\boldsymbol{t}-\boldsymbol{z}) \mathbf{B}$, with $\boldsymbol{t}$ a pre-image vector and $\boldsymbol{z}$ provided by a Gaussian Sampler. Chen and Chen [6] focus on masking the pre-image vector computation. In this work, we intend to mask the Gaussian Sampler. The signature algorithm is detailed in [25] in the corresponding section.

Gaussian Sampler The Gaussian Sampler is the composition built from the following functions:

ApproxExp. This function return $2^{63} \times c c s \times e^{-x}$ and depends of a matrix $C$ defined in page 42 of [25]:

```
Algorithm 1: ApproxExp(x,ccs) [25]
    Data: Foating-point values \(x \in[0, \ln (2)]\) and \(c c s \in[0,1]\)
    Result: An integral approximation of \(2^{63} \cdot c c s \cdot \exp (-x)\)
    \(y \leftarrow C[0] ; \quad / / y\) and \(z\) remain in \(\left\{0 \cdots 2^{63}-1\right\}\) the whole algorithm
    \(z \leftarrow\left\lfloor 2^{63} \cdot x\right\rfloor ;\)
    for \(i\) from 1 to 12 do
        \(y \leftarrow C[i]-(z \cdot y) \gg 63 ;\)
    \(z \leftarrow\left\lfloor 2^{63} \cdot c c s\right\rfloor ;\)
    \(y \leftarrow(z \cdot y) \gg 63\);
    return \(y\);
```

BerExp. This function return 1 with probability $c c s \times e^{-x}$ :

```
Algorithm 2: BerExp(x,ccs) [25]
    Data: Foating-point values \(x, c c s \geq 0\)
    Result: A single bit, equal to 1 with probability \(\approx c c s \cdot \exp (-x)\)
    \(1 s \leftarrow\lfloor x / \ln (2)\rfloor ; / /\) Compute the unique decomposition \(x=\ln \left(2^{s}\right)+r\) with
        \((r, s) \in[0, \ln (2)) \times \mathbb{Z}^{+}\)
    \(r \leftarrow x-s \cdot \ln (2) ;\)
    \(s \leftarrow \min (s, 63) ;\)
    \(z \leftarrow(2 \cdot \operatorname{ApproxExp}(r, c c s)-1) \gg s ;\)
    \(i \leftarrow 64 ;\)
    do
        \(i \leftarrow i-8 ;\)
        \(w \leftarrow \operatorname{UniformBits}(8)-((z \gg i) \& 0 \operatorname{XFF}) ;\)
    while \(((w=0)\) and \((i>0))\);
    return \(\llbracket w<0 \rrbracket\);
```

SamplerZ. The Gaussian Sampler:

```
Algorithm 3: SamplerZ \(\left(\mu, \sigma^{\prime}\right)\) [25]
    Data: Foating-point values \(\mu, \sigma^{\prime} \in \mathcal{R}\) such that \(\sigma^{\prime} \in\left[\sigma_{\min }, \sigma_{\max }\right]\)
    Result: \(z \in \mathbb{Z}\) sampled from a distribution very close to \(D_{\mathbb{Z}, \mu, \sigma^{\prime}}\)
    \(r \leftarrow \mu-\lfloor\mu\rfloor ;\)
    \(c c s \leftarrow \sigma_{\text {min }} / \sigma^{\prime} ;\)
    while 1 do
        \(z_{0} \leftarrow \operatorname{BASESAMPLER}()\);
        \(b \leftarrow \operatorname{UniformBits}(8) \& 0 x 1\);
        \(z \leftarrow b+(2 \cdot b-1) z_{0} ;\)
        \(x \leftarrow \frac{(z-r)^{2}}{2 \sigma^{\prime 2}}-\frac{z_{0}^{2}}{2 \sigma_{\text {max }}} ;\)
        if \(\operatorname{BErExp}(x, c c s)=1\) then
            return \(z+\lfloor\mu\rfloor ;\)
```

```
Algorithm 4: BaseSampler() [25]
    Data: -
    Result: An integer \(z_{0} \in\{0, \cdots, 18\}\) such that \(z \sim \chi\)
    \(u \leftarrow \operatorname{UniformBits}(72)\);
    \(z_{0} \leftarrow 0 ;\)
    for \(i\) from 0 to 17 do
        \(z_{0} \leftarrow z_{0}+\llbracket u<\operatorname{RCDT}[i] \rrbracket ;\)
    return \(z_{0}\);
```

where RCDT is defined in Falcon Specification [25].

### 2.4 Floor Function

The floor function is defined as follows:
Definition 1. $\forall x \in \mathbb{R}$, the floor function of $x$, denoted by $\lfloor x\rfloor$, returns the greatest integer $z$ such as $z \leq x$.
$\forall x \in \mathbb{R}$, the truncate function of $x=i . f,(i, f) \in \mathbb{Z} \times \mathbb{N}$, denoted by truncate $(x)$, returns $i$.

Binary64 Encoding A floating-point [16] is encoded with a sign bit $s$, a 11-bits long exponent $e$ and a 52-bits long mantissa $m$ such as:

$$
\begin{equation*}
x \in \mathbb{R}, x=(-1)^{s} \times 2^{e-1023} \times\left(1+m \times 2^{-52}\right) \tag{1}
\end{equation*}
$$

Computing The Floor Computing the floor function on a floating-point is performed by truncating the mantissa according to the value of the exponent and the sign:

- If $e<1023$ then if $s=0$ then $\lfloor x\rfloor=0$ else $\lfloor x\rfloor=-1$. Indeed,

$$
\begin{align*}
& (e<1023) \wedge(s=0) \Longrightarrow 0 \leq x \leq 2^{-1}+m \times 2^{-53}<1  \tag{2}\\
& (e<1023) \wedge(s=1) \Longrightarrow 0>x \geq-2^{-1}+-m \times 2^{-53} \geq-1 \tag{3}
\end{align*}
$$

- If $e>1074$ then $\lfloor x\rfloor=x$. We have

$$
\begin{align*}
e>1074 \Longrightarrow|x| & =2^{e-1023}+m \times 2^{e-1023-52}  \tag{4}\\
& =\left(2^{e-1023}\right) \in \mathbb{N}^{*}+\left(m \times 2^{e-1075}\right) \in \mathbb{N} \Longrightarrow x \in \mathbb{N}^{*} \tag{5}
\end{align*}
$$

The sign bit $s$ only changes $" \in \mathbb{N}$ " in " $\in \mathbb{Z}^{-} "$.

- If $1023 \leq e \leq 1074$ then we truncate the mantissa $m$ of $x$ and remove its $1074-e$ last bits $m^{[52-(e-1023): 1]}$. That way we have

$$
\begin{align*}
1023 \leq e \leq 1074 \Longrightarrow x & =2^{e-1023}+m^{[64: 1075-e]} \times 2^{52-(e-1023)+e-1023-52}  \tag{6}\\
& =\left(2^{e-1023}\right) \in \mathbb{N}^{*}+\left(m^{[64: 1075-e]}\right) \in \mathbb{N} \tag{7}
\end{align*}
$$

However, this only provides $\operatorname{truncate}(x)$. To get $\lfloor x\rfloor$, one has to take into account the sign bit $s$. We can rely on the fact that $\forall x \in \mathbb{R}^{-} \backsim \mathbb{Z}$, $\operatorname{truncate}(x)=$ $\lfloor x\rfloor+1$ and $\forall x \in \mathbb{R}^{+}$, truncate $(x)=\lfloor x\rfloor$. Thus, recovering the sign bit allows us to properly compute the floor function from the truncated one in this case.

Remark 1. To compute the truncate ( $x$ ) function, one can use the same method but discard the use of the sign. For the case $e<1023$, the result is always 0 .

This method requires the use of the exponent and the sign, which are both sensitive values. In this work, we propose a method to perform this truncation securely.

### 2.5 Masking

Masking is a generic countermeasure to SCA at the software level. Instead of processing a sensitive data, it is split into random shares which are processed separately, like in Boolean and Arithmetic masking [19]. Masking security can be evaluated thanks to the $t$-probing model, first introduced in [15]. A gadget is then said secured against $t$-order attacks if no information can be recovered by any set of $t$ intermediate values. However, for the composition of gadgets we use a stronger model introduced in [1]: the (Strong) Non-Interference model.

Definition 2. ( t -Non Interference ( $\mathrm{t}-\mathrm{NI}$ ) security [1]). A gadget is said t -Non Interference ( $\mathrm{t}-\mathrm{NI}$ ) secure if every set of t intermediate values can be simulated by no more than t shares of each of its inputs.
$t-N I$ gadgets composition does not imply $t-N I$ security. We need a stronger definition for this:

Definition 3. ( t -Strong Non Interference ( $\mathrm{t}-\mathrm{SNI}$ ) security [1]). A gadget is said t -Strong Non-Interference ( $\mathrm{t}-\mathrm{SNI}$ ) secure if for every set of $t_{I}$ of internal intermediate values and $t_{O}$ of its output shares with $t_{I}+t_{O} \leq \mathrm{t}$, they can be simulated by no more than $t_{I}$ shares of each of its inputs.

We use those models in Section 5 to demonstrate the security of our design. We rely on existing gadgets and propose new ones, as shown in Table 2.5.

## 3 Masking the Floor Function

In Section 2.4 we have described how to compute the floor using floating-point arithmetic. We present now the corresponding masking gadgets.

Remark 2. With small modifications, our design can also be used to compute the truncate and the rounding functions. As only the floor is required to protect FALCON, we provide their pseudo-codes in Appendix B.

Table 1. List of gadgets, their security and their reference

| Algorithm | Description | Security | Reference |
| :---: | :---: | :---: | :---: |
| SecAnd | AND of Boolean shares | $t$-SNI | [1],[15] |
| SecAdd | Addition of Boolean shares | $t$-SNI | [2],[8] |
| A2B | Arithmetic to Boolean conversion | $t$-SNI | [27] |
| B2A | Boolean to Arithmetic conversion | $t$-SNI | [4] |
| RefreshMasks | $t$-NI refresh of masks | $t-N I$ | [1], [4] |
| Refresh | $t$-SNI refresh of masks | $t$-SNI | [1] |
| SecOr | OR of Boolean shares | $t$-SNI | [6] |
| SecNonZero | NonZero check of shares | $t$-SNI | [6] |
| SecFprUrsh | Right-shift with sticky bit | $t$-SNI | [6] |
| SecFprNorm64 | Normalization to $\left[2{ }^{63}, 2^{64}\right)$ | $t-N I$ | [6] |
| SecFprAdd | Floating addition | $t-S N I$ | [6] |
| SecFprMul | Floating multiplication | $t$-SNI | [6] |
| SetExponentZero | Set exponent to zero | $t$-SNI | Algorithm 8 |
| SecFprUrsh ${ }_{f}$ | Right-shift without sticky bit | $t-S N I$ | Algorithm 7 |
| RemoveDecimal | Truncate the mantissa | $t$-SNI | Algorithm 6 |
| SecFprBaseInt | Compute the floor | $t$-SNI | Algorithm 5 |
| SecFprScalePow2 | Multiplies by a power of 2 | $t-S N I$ | Algorithm 11 |
| SecFprComp | Compares two values | $t-S N I$ | Algorithm 10 |
| SecFprInv | Inversion | $t-S N I$ | Algorithm 9 |

SecFprBaseInt $_{\mathbf{f}}$ : The gadget SecFprBaseInt ${ }_{f}$ (Algorithm 5) is the main function of the masked floor, the masked truncate, and the masked rounding. Gadgets and $\mathrm{Zero}_{f}$ are parametered ${ }^{4}$ by these functions.

We now focus on $f=$ floor. We first extract ${ }^{5}$ the data from the encoding used by [6] and place into three variables $s_{y}, e_{y}$, and $m_{y}$, which are directly linked to the output of the algorithm.

We first check if $c_{x}=e_{y}-$ Zero $_{f}<0$, corresponding to Equation 2. If $c x$ is negative, $|x|<1$ and all decimals can be removed by putting $m y=0$. The case $-1<x<0$ is solved by SetExponentZero (Algorithm 8) at the end of the algorithm. For the other cases, the mantissa is unchanged and we can cover the two remaining cases using RemoveDecimal (Algorithm 6).

[^0]```
Algorithm 5: \(\operatorname{SecFprBase}^{\operatorname{Int}} \mathrm{f}_{\mathrm{f}} \mathrm{x}\) )
    Data: 64 -bit boolean shares \(\left(x_{i}\right)_{1 \leq i \leq n}\) for value x
    Result: 64-bit boolean shares \(\left(y_{i}\right)_{1 \leq i \leq n}\) for mantissa value \(\mathrm{y}=\mathrm{f}(\mathrm{x})\).
    \(\left(\left(m y_{i}\right),\left(e y_{i}\right),\left(s y_{i}\right)\right) \leftarrow \operatorname{SecFprExtract}\left(\left(x_{i}\right)\right) ;\)
    \(\left(c x_{i}\right) \leftarrow\left(e y_{i}\right), c x_{1} \leftarrow e y_{1}-\) Zero \(_{f} ;\)
    \(\left(c_{i}\right) \leftarrow \mathrm{A} 2 \mathrm{~B}\left(\left(c x_{i}^{(16)}\right)\right) ;\)
    \(\left(m y_{i}\right) \leftarrow \operatorname{SecAnd}\left(\left(m y_{i}\right),\left(\neg\left(-c_{i}\right)\right)\right) ;\)
    \(\left(m y_{i}\right),\left(e y_{i}\right),\left(R n d_{i}\right) \leftarrow\)
    \(\operatorname{RemoveDecimal}_{f}\left(\left(m y_{i}\right),\left(e y_{i}\right), \operatorname{Refresh}\left(s y_{i}\right), \operatorname{Refresh}\left(\left(c x_{i}\right)\right)\right) ;\)
    \(\left(m y_{i}\right),\left(e y_{i}\right) \leftarrow\) SecFprNorm64 \(\left(\left(m y_{i}\right),\left(e y_{i}\right)\right)\);
    \(\left(m y_{i}\right) \leftarrow\left(m y_{i}^{[63: 11]}\right) ;\)
    \(e y_{1} \leftarrow e y_{1}+11 ;\)
    \(\left(e y_{i}\right),\left(s y_{i}\right) \leftarrow \operatorname{SetExponentZero}_{f}\left(\left(e y_{i}\right),\left(\neg\left(-c_{i}\right)\right),\left(s_{i}\right),\left(\right.\right.\) Rnd \(\left.\left._{i}\right)\right)\);
    \(\left(y_{i}^{(64)}\right) \leftarrow\left(s y_{i}\right),\left(y_{i}^{[63: 53]}\right) \leftarrow\left(e y_{i}\right),\left(y_{i}^{[53: 1]}\right) \leftarrow\left(m y_{i}\right) ;\)
    return \(\left(y_{i}\right)\);
```

As the algorithm RemoveDecimal does not normalize the mantissa, then we apply SecFprNorm64 (see [6] Algorithm 10 page 286) and compute a shifted $m y$ and $e y$ to set the mantissa back to bits [52:1] and update ey. Finally, the last step in the algorithm, before reformatting the initial encoding, consists in applying the specific encoding of " 0 " if it is the expected result. To do this, we apply the SetExponentZero $_{f}$ function (Algorithm 8).

```
Algorithm 6: RemoveDecimal \({ }_{\text {floor }}\left(\left(m y_{i}\right),\left(e y_{i}\right),\left(s y_{i}\right),\left(c x_{i}\right)\right)\)
    Data: 64-bit boolean shares \(\left(m y_{i}\right)_{1 \leq i \leq n}\) for mantissa value my;
    16 -bit arithmetic shares \(\left(e y_{i}\right)_{1 \leq i \leq n}\) for exponent value ey;
    1-bit boolean shares \(\left(s y_{i}\right)_{1 \leq i \leq n}\) for sign value sy
    16-bit arithmetic shares \(\left(c x_{i}\right)_{1 \leq i \leq n}\) for value cx \(=\mathrm{ex}\)-2013.
    Result: 64-bit boolean shares \(\left(m y_{i}\right)_{1 \leq i \leq n}\) for mantissa value
            \(m y \gg(52-c x) ;\)
    16 -bit arithmetic shares \(\left(e y_{i}\right)_{1 \leq i \leq n}\) for exponent value \(e y+(52-c x)\)
    \(c x_{1} \leftarrow c x_{1}-52 ;\)
    \(\left(c_{i}\right) \leftarrow \mathrm{A} 2 \mathrm{~B}\left(\left(c x_{i}\right)\right) ;\)
    \(\left(c p_{i}\right) \leftarrow\left(\left(c_{i}^{(16)}\right)\right) ;\)
    \(\left(c_{i}\right) \leftarrow \operatorname{SecAnd}\left(\operatorname{Refresh}\left(\left(c_{i}\right)\right),\left(-c p_{i}\right)\right) ;\)
    \(\left(c x_{i}\right) \leftarrow \mathrm{B} 2 \mathrm{~A}\left(\left(c_{i}\right)\right) ;\)
    \(\left(m y_{i}\right),\left(\operatorname{rot}_{i}\right) \leftarrow \operatorname{SecFprUrsh}_{f}\left(\left(m y_{i}\right),\left(-c x_{i}\right)\right) ;\)
    \(\left(b_{i}\right) \leftarrow \operatorname{SecNonZero}\left(\left(\operatorname{rot}_{i}\right)\right) ;\)
    \(\left(c p_{i}\right) \leftarrow \operatorname{SecAnd}\left(\left(c p_{i}\right),\left(s y_{i}\right)\right) ;\)
    \(\left(c p_{i}\right) \leftarrow \operatorname{Sec} \operatorname{And}\left(\left(c p_{i}\right),\left(b_{i}\right)\right) ;\)
    \(\left(m y_{i}\right) \leftarrow \operatorname{Sec} \operatorname{Add}\left(\left(m y_{i}\right),\left(c p_{i}\right)\right) ;\)
    \(\left(e y_{i}\right) \leftarrow\left(\operatorname{Refresh}\left(e y_{i}\right)-c x_{i}\right) ;\)
    return \(\left(\left(m y_{i}\right),\left(e y_{i}\right)\right)\);
```

RemoveDecimal ${ }_{\text {floor }}$ : $c x=e_{y}-1023<0$ (Equation 2) being covered, we use RemoveDecimal ${ }_{\text {floor }}$ (Algorithm 6) for the two remaining cases ${ }^{6}$, as described in Section 2.4. If $c x \geq 52$, then $x$ is an integer as shown in Equation 4. To avoid information loss during the remainder of SecFprBaseInt, we replace $c x$ by 0 . Finally, if $0 \leq c_{x} \leq 51$, the mantissa must be truncated accordingly.

We use a modification of the SecFprUrsh method from [6] (Algorithm 9 page 286 ) to shift the mantissa $m y$ by $c d=52-c x$. Our method, $\operatorname{SecFprUrsh}_{\mathrm{f}}$ (Algorithm 7 , does not keep the sticky bit but the removed part. Once the mantissa is shifted, we have consequently performed the truncate $(x)$ function. As described in Section 2.4, for the floor we also have to check whether the sign sy is 1 . In that case, we check by applying SecNonZero, with result denoted $b$, if the removed part is 0 . If so, we apply the floor function to a negative integer. Else, we have to retrieve 1 to the result in accordance with Section 2.4. We do so by securely adding $c p=s \wedge b$ to the shifted $m y$, as summed up in Table 2.

Table 2. Truth table of $c p=s \wedge b$ and interpretations

| $s y$ | $b$ | $c p=s y \wedge b$ | Interpretation |
| :---: | :---: | :---: | :--- |
| 0 | $b$ | 0 | $x$ is a positive real |
| 1 | 0 | 0 | $x$ is an negative integer |
| 1 | 1 | 1 | $x$ is an non-integer negative real |

SetExponentZero ${ }_{\text {floor }}$ : This last function (Algorithm 8) is necessary in the algorithm and uses the data collected throughout the calculations of the whole algorithm to modify $e y$ and $s y$ if the expected result is 0 . The encoding of 0 is special because it is encoded by itself. The desired result is zero only if $|x|<1$ and $s y=0$. We remind that floor $(x)=-1$ if $s y=1$ and $|x|<1 .-1$ is encoded as $s y=1, e y=1023$ and $m y=0$.

## 4 Application to Falcon : Gaussian Sampler

The floor function has been described above and we now address the SamplerZ function (Algorithm 3 or see [25] Algorithm 15 page 43). In the algorithms SamplerZ and BerExp (Algorithm 2 or see [25] Algorithm 14 page 43), division operations are used. Most of these divisions involve constants as the divisor, allowing us to pre-calculate the inverse and perform a multiplication. However, the first division in SamplerZ (line 2) involves a division with secret information. Hence, it is necessary to design a way to perform a division by an arbitrary value securely. To do so we choose to invert x and then compute a multiplication in

[^1]```
Algorithm 7: \(\operatorname{SecFprUrsh}_{\text {floor }}\left(\left(m y_{i}\right),\left(c x_{i}\right)\right)\)
    Data: 6 -bit arithmetic shares \(\left(c x_{i}\right)_{1 \leq i \leq n}\) for value cx;
    64 -bit boolean shares \(\left(m y_{i}\right)_{1 \leq i \leq n}\) for sign value \(m y\).
    Result: 64 -bit boolean shares \(\left(m y_{i}^{\prime}\right)_{1 \leq i \leq n}\) for value \(m y \gg c x\)
    64 -bit boolean shares \(\left(\operatorname{rot}_{i}\right)_{1 \leq i \leq n}\) for value \(m y^{[c x: 1]}\).
    \(\left(m_{i}\right)_{1 \leq i \leq n} \leftarrow((1 \ll 63), 0, \cdots, 0) ;\)
    for \(i\) from 1 to \(n\) do
        Right-Rotate ( \(m y_{i}\) ) by \(c x_{j}\);
        \(\left(m y_{i}\right) \leftarrow \operatorname{RefreshMasks}\left(\left(m y_{i}\right)\right) ;\)
        Right-Rotate ( \(m_{i}\) ) by \(c x_{j}\);
        \(\left(m_{i}\right) \leftarrow \operatorname{RefreshMasks}\left(\left(m_{i}\right)\right)\);
    len \(\leftarrow 1\);
    while len \(\leq 32\) do
        \(\left(m_{i}\right) \leftarrow\left(m_{i} \oplus\left(m_{i} \gg\right.\right.\) len \(\left.)\right) ;\)
        len \(\leftarrow\) len \(\ll 1\);
    \(\left(m y_{i}^{\prime}\right) \leftarrow \operatorname{SecAnd}\left(\left(m y_{i}\right),\left(m_{i}\right)\right) ;\)
    \(\left(\operatorname{rot}_{i}\right) \leftarrow \operatorname{SecAnd}\left(\left(m y_{i}\right),\left(\neg\left(m_{i}\right)\right)\right) ;\)
    return \(\left(\left(m y_{i}^{\prime}\right),\left(\right.\right.\) rot \(\left.\left._{i}\right)\right)\);
```

Table 3. Encoding 0 , minus 1 or others: Truth table

| $-s y$ | $b$ | $-s y \vee b$ | Interpretation |
| :---: | :---: | :---: | :---: |
| $0 \cdots 0$ | $0 \cdots 0$ | $0 \cdots 0$ | "Small" positive number $: e y=0$ and $s y=0$ |
| $1 \cdots 1$ | $0 \cdots 0$ | $1 \cdots 1$ | "Small" negative number $: e y=1023$ and $s y=1$ |
| $-s y$ | $1 \cdots 1$ | $01 \cdots 1$ | Non zero number $: e y=e y$ and $s y=s y$ |

```
Algorithm 8: SetExponentZerofloor \(\left(\left(e y_{i}\right),\left(s y_{i}\right),\left(b_{i}\right)\right)\)
    Data: 16-bit arithmetic shares \(\left(e y_{i}\right)_{1 \leq i \leq n}\) for exponent value ey;
    1-bit boolean shares \(\left(s y_{i}\right)_{1 \leq i \leq n}\) for sign value sy
    64 -bit boolean shares \(\left(b_{i}\right)_{1 \leq i \leq n}\).
    Result: 16-bit boolean shares \(\left(e y_{i}\right)_{1 \leq i \leq n}\) for exponent value \(e y+(52-c x)\);
    1-bit boolean shares \(\left(s y_{i}\right)_{1 \leq i \leq n}\) for sign value.
    \(\left(e y_{i}\right) \leftarrow \mathrm{A} 2 \mathrm{~B}\left(\left(e y_{i}\right)\right)\);
    \(\left(b_{i}^{\prime}\right) \leftarrow\left(-s y_{i}\right) ;\)
    \(\left(b_{i}^{\prime}\right) \leftarrow \operatorname{Sec} \operatorname{Or}\left(\left(b_{i}^{\prime}\right),\left(b_{i}\right)\right) ;\)
    \(\left(e y_{i}\right) \leftarrow \operatorname{SecAnd}\left(\left(e y_{i}, b_{i}^{\prime}\right)\right) ;\)
    \(\left(s y_{i}\right) \leftarrow \operatorname{SecAnd}\left(\left(s y_{i}, b_{i}^{\prime}\right)\right) ;\)
    return \(\left(\left(e y_{i}\right),\left(s y_{i}\right)\right)\);
```

order to divide. Computing the inverse involves performing a Euclidean division until obtaining sufficient precision ( 55 bits) to construct it.

Division : Remind that a binary64 value is represented by a tuple of three elements $(s, e, m$ ), where $s$ is a 1-bit sign, $e$ a 11-bit exponent and $m$ a 52-bit mantissa. We construct the result tuple ( $s_{y}, e_{y}, m_{y}$ ) considering each element separately. Inverse operation preserves the sign, so $s_{y}=s_{x}$. Then we consider the exponent. It consists in finding how many shift we require before starting to subtract information, hence we calculate $c_{x}=e_{x}-1023$. If the mantissa is zero, then $x$ is a power of 2 and $1 \ll c_{x}=x$, enabling us to perform the only subtraction necessary for the inverse calculation. However, if $m_{x} \neq 0,(1 \ll$ $\left.c_{x}\right)<x$, an additional shift is required. This is the reason for which we compute $\mathrm{b}=\operatorname{SecNonZero}\left(m_{x}\right)$ and write the final formula for $c_{x}$ as $c_{x}=e_{x}-1023+b$. By inversing this operation, we finally get:

$$
e_{\mathrm{inv}}=1023-c_{x}=2046-e_{x}-b
$$

The last step consists in finding the 52 bit-length mantissa. This part essentially corresponds to the Euclidean division: first, we compare our dividend $d=(1 \ll$ $c x)$ to $x$, by computing $\operatorname{comp}=\operatorname{SecFprComp}(d, x)$ (Algorithm 10). If $x<d$, then $\operatorname{comp}=1$, and this value needs to be carried over to the new mantissa. We then add $-x$ (just a sign modification) to d. If $\operatorname{comp}=0$, then we replace $x$ by 0 applying $\operatorname{SecAnd}(x, c o m p)$ and performing an addition with $d$, which does not alter the result. Finally, we shift d one time to the left to continue the division. After computing 53 bits ( 52 plus implicit bit) we calculate two additional bits, totaling 55 bits, to preserve the sticky bit. The method for calculating the sticky bit is available in the pseudo-code and is directly derived from SecFpr (see [6] Algorithm 11 page 287).

Comparison. In the inversion process, we must compare two masked binary64 values. We adapt the swap part of the SecFprAdd function (see [6] Algorithm 13 page 290). We add refresh operations to make the gadget t-SNI.

Minimum and Shift. BerExp (Alogirthm 2 or see [25] Algorithm 14 page 43) must be briefly described. It requires to multiply a number by $2^{-s}$, with $s$ masked after taking the minimum between $s$ and $63 . s$ must not be greater than 63 to retain its value, we can simply check if the exponent of $s$ is less than 1029 , same as checking if $\left(s_{i}^{(63)}\right)$ is equal to 1 .

To perform a shift, we extract the integer value of the masked $s$, then it is injected into the $\mathrm{SecFprUrsh}_{\mathrm{f}}$ function (Algorithm 7). More precisely we extract the mantissa of $s$, to which we add the implicit bit (unless $s=0$ ), and shift it by 46 to obtain the bits (information is in 6 bits). Then we shift again using $S_{\text {SecFprUrsh }}^{f}$ by es -1023 ( $1023<=$ es $<=1029$ or es $=0$ ). Finally we subtract the integer $s$ from the exponent of the $z$ that must be shifted.

```
Algorithm 9: \(\operatorname{SecFprInv}\left(\left(x_{i}\right)\right)\)
    Data: 64-bit boolean shares \(\left(x_{i}\right)_{1 \leq i \leq n}\) for value x .
    Result: 64 -bit boolean shares \(\left(y_{i}\right)_{1 \leq i \leq n}\) for value \(1 / x\)
    \(\left(s x_{i}\right),\left(e x_{i}\right),\left(m x_{i}\right) \leftarrow \operatorname{SecFprExtract}\left(\left(x_{i}\right)\right)\);
    \(\left(b_{i}\right) \leftarrow \operatorname{SecNonZero}\left(\left(m x_{i}\right)\right)\);
    \(\left(b a_{i}\right) \leftarrow \mathrm{B} 2 \mathrm{~A}\left(b_{i}\right) ;\)
    \(\left(e d_{i}\right) \leftarrow\left(e x_{i}+b a_{i}\right) ;\)
    \(\left(e y_{i}\right) \leftarrow\left(-e d_{i}\right) ;\)
    \(\left(e y_{i}\right) \leftarrow \mathrm{A} 2 \mathrm{~B}\left(\left(e y_{i}\right)\right), \quad\left(e d_{i}\right) \leftarrow \mathrm{A} 2 \mathrm{~B}\left(\left(e d_{i}\right)\right) ;\)
    \(\left(d_{i}\right) \leftarrow\left(e d_{i} \ll 52\right) ;\)
    \(\left(\right.\) minus \(\left.X_{i}\right) \leftarrow \operatorname{Or}\left(\left(2^{63}, 0, \cdots, 0\right),\left(x_{i}\right)\right) ;\)
    for \(j\) from 1 to 55 do
        \(\left(\operatorname{comp}_{i}\right) \leftarrow \operatorname{SecFprComp}\left(\left(x_{i}\right),\left(d_{i}\right)\right) ;\)
        \(\left(m y_{i}\right) \leftarrow\left(m y_{i} \oplus\left(c^{\prime} m p_{i} \ll(63-j)\right)\right) ;\)
        \(\left(x c p y_{i}\right) \leftarrow \operatorname{SecAnd}\left(\left(\right.\right.\) minus \(\left._{i}\right),-\left(\right.\) comp \(\left.\left._{i}\right)\right)\);
        \(\left(d_{i}\right) \leftarrow \operatorname{SecFprAdd}\left(\left(x c p y_{i}\right),\left(d_{i}\right)\right) ;\)
        \(\left(d_{i}\right) \leftarrow \operatorname{SecFprScalPtwo}\left(\left(d_{i}\right), 1\right) ;\)
    \(\left(m y_{i}\right) \leftarrow \operatorname{SecAnd}\left(\left(m y_{i}\right),-\left(b_{i}\right)\right) ;\)
    \(\left(y_{i}^{(64)}\right) \leftarrow \operatorname{Refresh}\left(\left(s y_{i}\right)\right),\left(y_{i}^{[63: 53]}\right) \leftarrow\left(e y_{i}\right),\left(y_{i}^{[52: 1]}\right) \leftarrow\left(m y_{i}^{[54: 3]}\right) ;\)
    \(\left(f_{i}\right) \leftarrow \operatorname{Sec} \operatorname{Or}\left(\operatorname{Refresh}\left(m y_{i}^{(1)}\right),\left(m y_{i}^{(3)}\right)\right) ;\)
    \(\left(f_{i}\right) \leftarrow \operatorname{SecAnd}\left(\left(f_{i}\right),\left(m y_{i}^{(2)}\right)\right) ;\)
    \(\left(y_{i}\right) \leftarrow \operatorname{SecAdd}\left(\left(y_{i}\right),\left(f_{i}\right)\right) ;\)
    return ( \(y_{i}\) );
```

```
Algorithm 10: \(\operatorname{SecFprComp}\left(\left(x_{i}\right),\left(y_{i}\right)\right)\)
    Data: 64 -bit boolean shares \(\left(x_{i}\right)_{1 \leq i \leq n}\) for value x;
    64-bit boolean shares \(\left(y_{i}\right)_{1 \leq i \leq n}\) for sign value \(y\).
    Result: 1-bit boolean shares \(\left(\operatorname{comp}_{i}\right)_{1 \leq i \leq n}\) for value \(\llbracket x<y \rrbracket\)
    \(\operatorname{Refresh}\left(\left(x_{i}\right)\right)\);
    \(\left(m x_{i}\right) \leftarrow\left(x_{i}^{[63: 1]}\right), \quad\left(m y_{i}\right) \leftarrow\left(y_{i}^{[63: 1]}\right) ;\)
    \(\left(d_{i}\right) \leftarrow \operatorname{Sec} \operatorname{Add}\left(\left(m x_{i}\right),\left(\neg m y_{1}, m y_{2}, \cdots, m y_{n}\right)\right) ;\)
    Refresh \(\left(\left(d_{i}\right)\right)\);
    \(\left(b_{i}\right) \leftarrow \operatorname{SecNonZero}\left(\left(\neg d_{1}, d_{2}, \cdots, d_{n}\right)\right) ;\)
    \(\left(b_{i}^{\prime}\right) \leftarrow \operatorname{SecNonZero}\left(\left(\neg\left(d_{1} \oplus 2^{63}\right), d_{2}, \cdots, d_{n}\right)\right) ;\)
    \(\left(c o m p_{i}\right) \leftarrow\left(d_{i}^{(63)} \oplus b_{i} \oplus b_{i}^{\prime}\right) ;\)
    return \(\left(c o m p_{i}\right)\);
```

Our function SecFprScalPow2 (Algorithm 11) handles the case of shifting by an unmasked integer value by simply performing a left shift if pow ${ }_{2}$ is positive and a right shift if it is negative, then we remove the useless information applying the truncation function ${ }^{7}$. Using this algorithm, division and multiplication by two are cheaper to perform.

```
Algorithm 11: SecFprScalPow2 \(\left(\left(x_{i}\right), p\right)\)
    Data: 64-bit boolean shares \(\left(x_{i}\right)_{1 \leq i \leq n}\) for value x ;
    An integer \(p\).
    Result: 64 -bit boolean shares \(\left(y_{i}\right)_{1 \leq i \leq n}\) for value \(x \times 2^{p}\)
    \(\left(s x_{i}\right),\left(e x_{i}\right),\left(m x_{i}\right) \leftarrow \operatorname{SecFprExtract}\left(\left(x_{i}\right)\right)\);
    \(\left(b_{i}\right) \leftarrow \operatorname{SecNonZero}\left(\left(x_{i}\right)\right)\);
    \(\left(e x_{i}\right) \leftarrow \mathrm{B} 2 \mathrm{~A}\left(\left(e x_{i}\right)\right) ;\)
    \(e x_{1} \leftarrow e x_{1}+p ;\)
    \(\left(e x_{i}\right) \leftarrow \mathrm{A} 2 \mathrm{~B}\left(\left(e x_{i}\right)\right) ;\)
    \(\left(e y_{i}\right) \leftarrow \operatorname{SecAnd}\left(\left(e x_{i}\right),-\left(b_{i}\right)\right) ;\)
    \(\left(y_{i}^{(64)}\right) \leftarrow\left(s y_{i}\right),\left(y_{i}^{[63: 53]}\right) \leftarrow\left(e y_{i}\right),\left(y_{i}^{[53: 1]}\right) \leftarrow\left(m y_{i}\right) ;\)
    return Refresh \(\left(y_{i}\right)\);
```


## 5 Security Proof

In this section we cover the $t$-SNI security of our design with $n=t+1$ shares. We follow and rely on the same principles used by Chen and Chen [6] for our proofs. We aim to propose only $t$-SNI secure gadgets as they are composable. It limits the risks of compositional flaws. We are aware that it leads to performance overheads and more demanding randomness requirements.

### 5.1 Floor Function

Lemma 1. The gadget SetExponentZerofoor (Algorithm 8) is t-SNI secure.

Proof. We use an abstract diagram in Figure 1 for our demonstration. The gadget only contains $t-S N I$ gadgets. By composition of $t-S N I$ gadgets, this gadget is itself $t-S N I$.

Lemma 2. The gadget $\boldsymbol{S e c F p r} \boldsymbol{U r s h} \boldsymbol{f l o o r}$ (Algorithm 7) is t-SNI secure.

Proof. The gadget SecFprUrsh floor is a slight modification of the gadget SecFprUrsh from [6]. Our gadget does not compute the sticky bit but retains the rotated out information. We rely on their proof regarding the $t$-SNI security of

[^2]

Fig. 1. Abstract diagram of SetExponentZero ${ }_{\text {floor }}$


Fig. 2. Abstract diagram of SecFprUrsh floor
the gadget Rotate (see [6], Lemma 3 and Figure 2). We now show that the operations below the rotation loop are $t-S N I$ secure. We use an abstract diagram in Figure 2 for the demonstration. Let an adversary probes the intermediate values sets $P_{1}$ of SecAnd, $P_{2}$ of SecAnd and $P_{3}$ of XOR. As SecAnd is $t$-SNI secure, one can use the sets $S_{2}^{1}, S_{2}^{2}$ (resp. $S_{1}^{1}, S_{1}^{2}$ ) to simulate $P_{2}$ (resp. $P_{1}$ ) and the ouput shares of (rot) (resp. (xi)>>(ci)) with sizes no more than $P_{2}$ (resp. $P_{1}$ ). One can simulate the probing set of $P_{3}$ in the XOR and the simulation sets $S_{2}^{2}$ and $S_{1}^{2}$ with the output shares $S_{3}$ of the rotation of $(\mathrm{mi})$. Indeed, as the XOR is a linear operation performed on each share separately, it is $t-N I$ secure. All probes are now simulated with output shares $S_{1}^{1} \cup S_{2}^{1}$ of the rotation of $(x i)$ and $S_{3}$ of the rotation of $(m i)$. We have $\left|S_{1}^{1} \cup S_{2}^{1}\right| \leq\left|P_{1}\right|+\left|P_{2}\right|$ and $\left|S_{3}\right| \leq\left|P_{3}\right|+\left|S_{2}^{2}\right|+\left|S_{1}^{2}\right| \leq\left|P_{3}\right|+\left|P_{2}\right|+\left|P_{1}\right|$. Along with the internal probes $P_{5}$ and $P_{4}$ from the rotation loop, all gadgets can be simulated by input shares with no more than $t_{I}$ values due to the $t-S N I$ security showed at first in ([6], Lemma $3)$.

Lemma 3. The gadget RemoveDecimalfloor (Algorithm 6) is t-SNI secure.

Proof. We use an abstract diagram in Figure 3 for the demonstration. We assume an adversary probes the intermediate values sets of the output shares $O$ and $P_{i}$ in each gadget for $i \in \llbracket 1 ; 12 \rrbracket$. We use simulation sets $S_{i}^{j}$ to simulate the values


Fig. 3. Abstract diagram of RemoveDecimal floor
for each gadget. $t$-SNI security implies that: if the size of all probing sets $P_{i}$ is $t_{I} \leq t$ and if the size of values required to simulate in each gadget is smaller than $t$, then the simulation sets linked to the input shares are not bigger than $t_{I}$. The $t$-SNI gadgets imply $|S| \leq|P|$ and the $t$-NI gadgets imply $|S| \leq|P|+|O|$. As Refresh, SecAnd, SecNonZero, SecFprUrsh ${ }_{\text {floor }}$, B2A and A2B are all $t$-SNI secure whereas SecAdd and " + " are $t-N I$ secure, we can sequentially derive the following:

$$
\begin{array}{ll}
-\left|S_{1}\right| \leq\left|P_{1}\right| & -\left|S_{8}\right| \leq\left|P_{8}\right| \\
-\left|S_{2}^{1}\right|,\left|S_{2}^{2}\right| \leq\left|P_{2}\right|+\left|O_{(e y)}\right| & -\left|S_{9}^{1}\right|,\left|S_{9}^{2}\right| \leq\left|P_{9}\right| \\
-\left|S_{4}^{1}\right|,\left|S_{4}^{2}\right| \leq\left|P_{4}\right|+\left|O_{(m y)}\right| & -\left|S_{10}^{1}\right|,\left|S_{10}^{2}\right| \leq\left|P_{10}\right| \\
-\left|S_{5}^{1}\right|,\left|S_{5}^{2}\right| \leq\left|P_{5}\right| & -\left|S_{11}\right| \leq\left|P_{11}\right| \\
-\left|S_{6}\right| \leq\left|P_{6}\right| & -\left|S_{12}\right| \leq\left|P_{12}\right|
\end{array}
$$

Based on the previous inequalities, we know that no gadget requires more than $t_{I}+t_{O}=t$ values to be simulated. This above method can be applied to the input shares as well, with $\left|S_{10}^{1}\right| \leq\left|P_{10}\right|$ for $(s y),\left|S_{7}^{1}\right| \leq\left|P_{7}\right|$ for $(m y),\left|S_{12}\right| \leq\left|P_{12}\right|$ for $(c x)$ and $\left|S_{2}^{1}\right| \leq\left|P_{2}\right|+\left|S_{1}\right| \leq\left|P_{2}\right|+\left|P_{1}\right|$ for (ey), no sizes being more than $t_{I}$.

Theorem 1. The gadget SecFprBaseInt floor (Algorithm 5) is t-SNI secure.

Proof. We use the same method as for the demonstration of Lemma 3. We use an abstract diagram in Figure 4 for the demonstration. Let assume an adversary probes the intermediate values sets of the output shares $O$ and $P_{i}$ in each


Fig. 4. Abstract diagram of SecFprBaseInt floor
gadget for $i \in \llbracket 1 ; 6 \rrbracket$. We use simulation sets $S_{i}^{j}$ to simulate the values for each gadget. $t$-SNI security implies that if the size of all probing sets $P_{i}$ is $t_{I} \leq t$ and if the size of values required to simulate in each gadget is smaller than $t$, then the simulation sets linked to the input shares are not bigger than $t_{I}$. As SetExponentZero, RemoveDecimal, SecAnd, A2B and Refresh are all $t-S N I$ secure while SecFprNorm64 is $t-N I$ secure, we can sequentially derive the following:
$-\left|S_{1}^{1}\right|,\left|S_{1}^{2}\right|,\left|S_{1}^{3}\right| \leq\left|P_{1}\right|$
$-\left|S_{4}^{1}\right|,\left|S_{4}^{2}\right| \leq\left|P_{4}\right|$
$-\left|S_{2}^{1}\right|,\left|S_{2}^{2}\right| \leq\left|P_{2}\right|+\left|O_{(m y)}\right| \quad-\left|S_{5}\right| \leq\left|P_{5}\right|$
$-\left|S_{3}^{1}\right|,\left|S_{3}^{2}\right|,\left|S_{3}^{3}\right|,\left|S_{3}^{4}\right| \leq\left|P_{3}\right| \quad-\left|S_{6}\right| \leq\left|P_{6}\right|$
$-\left|S_{6}\right| \leq\left|P_{6}\right|$

Based on the previous inequalities, we know that no gadget requires more than $t_{I}+\left|O_{(m y)}\right| \leq t$ values to be simulated. The above method is also applied to the input shares, with $\left|S_{4}^{1}\right| \leq\left|P_{4}\right|$ for $(m y),\left|S_{5} \cup S_{3}^{2} \cup S_{6}\right| \leq\left|P_{5}\right|+\left|P_{3}\right|+\left|P_{6}\right|$ for (ey) and $\left|S_{3}^{4} \cup S_{1}^{3}\right| \leq\left|P_{3}\right|+\left|P_{1}\right|$ for (sy), none being more than $t_{I}$.

### 5.2 Inverse

Lemma 4. The gadget SecFprComp (Algorithm 10) is t-SNI secure.

Proof. We use an abstract diagram in Figure 5 for our demonstration. This gadget is similar to the swap part of the SecFprAdd gadget from [6] (Theorem 3 , first part of the proof). We add some Refresh to ensure the $t$-SNI property. Note that the XOR associated to the probing set $P_{1}$ is $t-N I$ secure as this linear operation is performed on each share separately. The gadget SecAdd associated to the probe $P_{5}$ is also $t-N I$ secure. The other gadgets are $t-S N I$ secure. Hence, we have the following inequalities:


Fig. 5. Abstract diagram of SecFprComp

$$
\begin{array}{ll}
-\left|S_{1}^{0}\right|,\left|S_{1}^{1}\right|,\left|S_{1}^{2}\right| \leq\left|P_{1}\right|+\left|O_{(\text {comp })}\right| & -\left|S_{4}\right| \leq\left|P_{4}\right| \\
-\left|S_{2}\right| \leq\left|P_{2}\right| & -\left|S_{5}^{0}\right|,\left|S_{5}^{1}\right| \leq\left|P_{5}\right|+\left|S_{4}\right| \leq\left|P_{5}\right|+\left|P_{4}\right| \\
-\left|S_{3}\right| \leq\left|P_{3}\right| & -\left|S_{6}\right| \leq\left|P_{6}\right|
\end{array}
$$

According to these inequalities, no gadget requires more than $t_{I}+\left|O_{(c o m p)}\right| \leq t$ values to be simulated. This method can be applied to the input shares: For $\left(x_{i}\right)$, we have $\left|S_{6}\right| \leq\left|P_{6}\right| \leq t_{I}$ and for $\left(y_{i}\right)$ we have $\left|S_{5}^{0}\right| \leq\left|P_{5}\right|+\left|P_{4}\right| \leq t_{I}$.

Lemma 5. The gadget SecFprScalePow2 (Algorithm 11) is t-SNI secure.


Fig. 6. Abstract diagram of SecFprScalePow2

Proof. We use an abstract diagram in Figure 6 for our demonstration. This gadget mainly affects the exponent shares (ey). Apart from " + " which is $t$-NI as it is simply adding a constant to one share, all other gadgets are $t$-SNI. As the single input of the gadget " + " comes from a $t-S N I$ gadget $\mathbf{B 2 A}$ and then has its single output fed into another $t-S N I$ gadget, the chain B2A $\rightarrow "+" \rightarrow$ A2B is itself $t-S N I$. By composition, the entire gadget is $t-S N I$.

Theorem 2. The gadget SecFprInv (Algorithm 9) is t-SNI secure.


Fig. 7. Abstract diagram of LOOP

Proof. We use an abstract diagram in Figure 8 for our demonstration. We first prove that the gadget LOOP associated to the probes set $P_{5}$ is $t-S N I$ secure. We use an abstract diagram in Figure 7 for our demonstration. This gadget composes $t$-SNI gagdets, including SecFprComp and SecFprScalePow2, proven $t$-SNI in Lemmas 4 and 5. As the first iteration of the loop is $t$-SNI secure by composition, and the loop cycles on itself, all remaining iterations are also $t$-SNI secure. This implies the gadget LOOP is itself $t-S N I$ secure.
For the rest of the SecFprInv gadget, all gadgets are $t-S N I$ apart from + associated to the probes set $P_{7}$ and $\mathbf{S e c A d d}$ associated to the probes set $P_{1}$. We can derive the following:
$-\left|S_{1}\right| \leq\left|P_{1}\right|+\left|O_{\left(x_{-} i n v\right)}\right|$
$-\left|S_{8}\right| \leq\left|P_{8}\right|$
$-\left|S_{2}^{0}\right|,\left|S_{2}^{1}\right| \leq\left|P_{2}\right|$
$-\left|S_{3}^{0}\right|,\left|S_{3}^{1}\right| \leq\left|P_{3}\right|$
$-\left|S_{4}\right| \leq\left|P_{4}\right|$
$-\left|S_{5}^{0}\right|,\left|S_{5}^{1}\right| \leq\left|P_{5}\right|$
$-\left|S_{6}^{0}\right|,\left|S_{6}^{1}\right|,\left|S_{6}^{2}\right| \leq\left|P_{6}\right|$
$-\left|S_{7}\right| \leq\left|P_{7}\right|$
$-\left|S_{9}^{0}\right|,\left|S_{9}^{1}\right| \leq\left|P_{9}\right|+\left|S_{2}\right|+\left|S_{8}\right| \leq$ $\left|P_{9}\right|+\left|P_{2}\right|+\left|P_{8}\right|$
$-\left|S_{10}\right| \leq\left|P_{10}\right|$
$-\left|S_{11}\right| \leq\left|P_{11}\right|$
$-\left|S_{12}\right| \leq\left|P_{12}\right|$

Based on these inequalities, we know that no gadgets requires more than $t_{I}+\left|O_{\left(x_{\_} i n v\right)}\right| \leq t$ values to be simulated. This method can also be applied to the input shares: For (xi) we have $\left|S_{11} \cup S_{6}^{1}\right| \leq\left|P_{11}\right|+\left|P_{6}\right| \leq t_{I}$, for (exi) we have $\left|S_{9}^{1}\right| \leq\left|P_{9}\right|+\left|P_{8}\right|+\left|P_{2}\right| \leq t_{I}$, for (sxi) we have $\left|S_{12}\right| \leq\left|P_{12}\right| \leq t_{I}$ and for (mi) we have $\left|S_{6}^{2}\right| \leq\left|P_{6}\right| \leq t_{I}$.

## 6 Performances

Some results are shown in Table 4. This implementation is not optimized and is realized with a personal computer equipped with an Intel Core i7-11800H CPU. The compiler used is gcc version 9.4 .0 with options -O3. We have considered our performances of SecFprAdd and SecFprMul as reference and compare our work with the one of Chen and Chen [6], as they used a different hardware (Intel


Fig. 8. Abstract diagram of SecFprInv

Core i9-12900KF). We have designed our code around 3 shares and some wellknown optimizations for 2 shares masking have not been implemented. Hence, we observe that the complexity increases linearly with the number of shares.

Table 4. Time in microseconds

| Algorithm | $[25]$ | 2 Shares | 3 Shares |
| :--- | :--- | :--- | :--- |
| SecFprAdd [6] | 0.00011 | 7.533 | 13.552 |
| SecFprMul [6] $^{\text {S }}$ | 0.00014 | 5.563 | 11.622 |
| SecFprBaseInt $_{\text {floor }} 0.000136$ | 7.084 | 13.284 |  |
| SecFprUrsh $_{\text {floor }}$ | - | 0.113 | 0.219 |
| SecFprInv $^{\text {SecFprComp }}$ | 0.000138 | - | 559.658 |
| SecFprScalPwo2 | - | 1.601 | 994.416 |
| ApproxExp | 0.000126 | 0.943 | 2.471 |
| BerExp | 0.005446 | 190.207 | 1.903 |
| SamplerZ | 0.114 | 227.187 | 367.245 |
| 1024 SamplerZ $^{8}$ | 122.962 | 1807.353 | 441.951 |
| 2048 SamplerZ $^{9}$ | 247.902 | 1850633 | 4205.701 |

To replicate the performances of the calls to the Gaussian Sampler by FALCON, we performed SamplerZ by the same amount of iterations required in both

FALCON-512 and FALCON-1024. Table 4 highlights the impact of the division computation on SamplerZ. The SecFprInv gadget is the main bottleneck of our design as it involves 55 SecFprAdd . On the other hand, our SecFprBaseInt ${ }_{\text {floor }}$ gadget is no more costly than one SecFprAdd.

## 7 Conclusion

In this paper we have extended the work of Chen and Chen [6] and have used their gadgets and our new own gadgets to mask the floor function (Section 3). The Gaussian sampler of FALCON (Section 4) has been protected with this floor gadget. Additionally, to reach this task, we provided a masked implementation of the division (Section 4). We discussed about the $t$-SNI properties of our gadgets (Section 5). Finally, we provided some performances got on a personal computer equipped with an Intel Core CPU (Section 6).
Future works could lead to a complete masked implementation of the FALCON signature relying both on Chen and Chen [6] and our work. Improving the division should lead to better performances, as it is the main bottleneck in our current design. New masking methods for floating-point arithmetic, less reliant on A2B and B2A conversions, could be studied. Finally, fault-injection resilient designs could be of interest.

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## A SecFprExtract

```
Algorithm 12: SecFprExtract(x)
    Data: 64 -bit boolean shares \(\left(x_{i}\right)_{1 \leq i \leq n}\) for value x
    Result: 64-bit boolean shares \(\left(m x_{i}\right)_{1 \leq i \leq n}\) for mantissa value mx ;
    16 -bit arithmetic shares \(\left(e x_{i}\right)_{1 \leq i \leq n}\) for exponent value ex;
    1-bit boolean shares \(\left(s x_{i}\right)_{1 \leq i \leq n}\) for sign value s.
    \(\left(m x_{i}\right) \leftarrow\left(x_{i}^{[52: 1]}\right) ;\)
    \(\left(m x_{i}\right) \leftarrow \operatorname{Sec} \operatorname{Add}\left(\left(m x_{i}\right),\left(2^{52}, 0, \cdots, 0\right)\right) ; / /\) add implicit bit in the
    mantissa
    \(\left(e x_{i}\right) \leftarrow\left(x_{i}^{[63: 53]}\right) ;\)
    \(\left(e x_{i}\right) \leftarrow \mathrm{B} 2 \mathrm{~A}\left(\left(e x_{i}\right)\right) ;\)
    \(\left(s x_{i}\right) \leftarrow\left(x_{i}^{(64)}\right) ;\)
    return \(\left(\left(m x_{i}\right),\left(e x_{i}\right),\left(s x_{i}\right)\right)\);
```

This algorithm provides us the extraction of $(s x, e x, m x)$ by converting the 11bit boolean shares ex into 16 -bit arithmetic shares and adding the implicit bit to the mantissa $m x$. This bit which is not present in the $x$ information (in order to save one bit and gain in precision), is nevertheless very important. It especially enables us to normalize our shares correctly.

## B Alternate Algorithms For Truncate and Rounding

## B. 1 Truncature Function: Gadgets

```
Algorithm 13: RemoveDecimal \({ }_{\text {trunc }}\left(\left(m y_{i}\right),\left(e y_{i}\right),\left(s y_{i}\right),\left(c x_{i}\right)\right)\)
    Data: 64-bit boolean shares \(\left(m y_{i}\right)_{1 \leq i \leq n}\) for mantissa value my;
    16 -bit arithmetic shares \(\left(e y_{i}\right)_{1 \leq i \leq n}\) for exponent value ey;
    1-bit boolean shares \(\left(s y_{i}\right)_{1 \leq i \leq n}\) for sign value sy
    16 -bit arithmetic shares \(\left(c x_{i}\right)_{1 \leq i \leq n}\) for value cx \(=\mathrm{ex}-2013\).
    Result: 64 -bit boolean shares \(\left(m y_{i}\right)_{1 \leq i \leq n}\) for mantissa value
            \(m y \gg(52-c x)\);
    16 -bit arithmetic shares \(\left(e y_{i}\right)_{1 \leq i \leq n}\) for exponent value \(e y+(52-c x)\);
    \(c x_{1} \leftarrow c x_{1}-52 ; \quad / /\) check if \(0 \leq \mathrm{c}<51\)
    \(\left(c_{i}\right) \leftarrow \mathrm{A} 2 \mathrm{~B}\left(\left(c x_{i}\right)\right) ;\)
    \(\left(c p_{i}\right) \leftarrow\left(c_{i}^{(16)}\right) ;\)
    Refresh \(\left(\left(c_{i}\right)\right)\);
    \(\left(c_{i}^{\prime}\right) \leftarrow\left(-c p_{i}\right) ; \quad / /\) if \(\mathrm{cp}=0 \mathrm{cx}=0\). if not \(\mathrm{cx}=\mathrm{cx}\)
    \(\left(c_{i}\right) \leftarrow \operatorname{SecAnd}\left(\left(c_{i}\right),\left(c p_{i}\right)\right) ;\)
    \(7\left(c x_{i}\right) \leftarrow \mathrm{B} 2 \mathrm{~A}\left(\left(c_{i}\right)\right)\);
    \(8\left(c d_{i}\right) \leftarrow\left(-c x_{i}\right)\);
    \(\left(m y_{i}\right) \leftarrow{\operatorname{SecFpr} \operatorname{Ursh}_{f}\left(\left(m y_{i}\right),\left(c d_{i}\right)\right) ; \quad / / m y \gg 52-c x}\)
    \(\left(e y_{i}\right) \leftarrow\left(\operatorname{Refresh}\left(e y_{i}\right)+c d_{i}\right) ;\)
    return \(\left(\left(m y_{i}\right),\left(e y_{i}\right)\right)\);
```

```
Algorithm 14: SetExponentZerotrunc \(\left(\left(e y_{i}\right),\left(s y_{i}\right),\left(b_{i}\right)\right)\)
    Data: 16-bit arithmetic shares \(\left(e y_{i}\right)_{1 \leq i \leq n}\) for exponent value ey;
    1-bit boolean shares \(\left(s y_{i}\right)_{1 \leq i \leq n}\) for sign value sy
    64 -bit boolean shares \(\left(b_{i}\right)_{1 \leq i \leq n}\).
    Result: 16-bit boolean shares \(\left(e y_{i}\right)_{1 \leq i \leq n}\) for exponent value
            \(e y+(52-c x) ;\)
    1-bit boolean shares \(\left(s y_{i}\right)_{1 \leq i \leq n}\) for sign value.
    \(\left(e y_{i}\right) \leftarrow \mathrm{A} 2 \mathrm{~B}\left(\left(e y_{i}\right)\right) ;\)
    \(2\left(e y_{i}\right) \leftarrow \operatorname{SecAnd}\left(\left(e y_{i}, b_{i}\right)\right)\);
    \(3\left(s y_{i}\right) \leftarrow \operatorname{SecAnd}\left(\left(s y_{i}, b_{i}\right)\right)\);
    return \(\left(\left(e y_{i}\right),\left(s y_{i}\right)\right)\);
```

For truncature function, 0 is the result if $e x-1023<0$. So SetExponentZero ${ }_{\text {trunc }}$ sets $e y$ and sy to 0 depending only on $\left(b_{i}\right)$, the zero condition.

## B. 2 Rounding Function: Gadgets

```
Algorithm 15: RemoveDecimal \({ }_{\text {round }}\left(\left(m y_{i}\right),\left(e y_{i}\right),\left(s y_{i}\right),\left(c x_{i}\right)\right)\)
    Data: 64-bit boolean shares \(\left(m y_{i}\right)_{1 \leq i \leq n}\) for mantissa value my;
    16 -bit arithmetic shares \(\left(e y_{i}\right)_{1 \leq i \leq n}\) for exponent value ey;
    1-bit boolean shares \(\left(s y_{i}\right)_{1 \leq i \leq n}\) for sign value sy
    16 -bit arithmetic shares \(\left(c x_{i}\right)_{1 \leq i \leq n}\) for value \(\mathrm{cx}=\mathrm{ex}-2013\).
    Result: 64-bit boolean shares \(\left(m y_{i}\right)_{1 \leq i \leq n}\) for mantissa value
                \(m y \gg(52-c x)\);
    16 -bit arithmetic shares \(\left(e y_{i}\right)_{1 \leq i \leq n}\) for exponent value \(e y+(52-c x)\);
    1-bit boolean shares \(\left(R n d_{i}\right)\) for value Rnd - the bit at position -1.
    \(c x_{1} \leftarrow c x_{1}-53 ; \quad \quad / /\) check if \(0 \leq \mathrm{c}<51\)
    \(\left(c_{i}\right) \leftarrow \mathrm{A} 2 \mathrm{~B}\left(\left(c x_{i}\right)\right) ;\)
    \(\left(c p_{i}\right) \leftarrow\left(c_{i}^{(16)}\right) ;\)
    \(\left(\right.\) rshORnot \(\left._{i}\right) \leftarrow \operatorname{Refresh}(-c p[i]) ;\)
    \(\left(c_{i}^{\prime}\right) \leftarrow\left(-c p_{i}\right) ; \quad / /\) if \(\mathrm{cp}=0 \mathrm{cx}=0\). if not \(\mathrm{cx}=\mathrm{cx}\)
    \(c x_{1} \leftarrow c x_{1}+1 ;\)
    \(\left(c_{i}\right) \leftarrow \mathrm{A} 2 \mathrm{~B}\left(\left(c x_{i}\right)\right) ;\)
    \(\left(c_{i}\right) \leftarrow \operatorname{Sec} \operatorname{And}\left(\left(c_{i}\right),\left(c p_{i}\right)\right) ;\)
    \(\left(c x_{i}\right) \leftarrow \mathrm{B} 2 \mathrm{~A}\left(\left(c_{i}\right)\right) ;\)
    \(\left(c d_{i}\right) \leftarrow\left(-c x_{i}\right) ;\)
    \(\left(m y_{i}\right) \leftarrow \operatorname{SecFprUrsh}\left(\left(m y_{i}\right),\left(c d_{i}\right)\right) ; \quad / / m y \gg 53-c x\)
    \(\left(R n d_{i}\right) \leftarrow\left(m y_{i}^{(1)}\right) ;\)
    \(\left(\right.\) Rnd \(\left._{i}\right) \leftarrow \operatorname{SecAnd}\left(\left(\right.\right.\) Rnd \(\left._{i}\right),\left(\right.\) rshORnot \(\left.\left._{i}\right)\right) ;\)
    \(\left(m y 1_{i}\right) \leftarrow\left(m y_{i}\right) \gg 1, \quad\left(e 1_{i}\right) \leftarrow(1,0, \cdots, 0) ;\)
    \(\left(m y 2_{i}\right) \leftarrow\left(m y_{i}\right) \quad, \quad\left(e 2_{i}\right) \leftarrow(0, \cdots, 0) ;\)
    \(\left(m y 1_{i}\right) \leftarrow \operatorname{SecAnd}\left(\left(m y 1_{i}\right),\left(\right.\right.\) rshORnot \(\left.\left._{i}\right)\right) ;\)
    \(\left(e 1_{i}\right) \leftarrow \operatorname{SecAnd}\left(\left(e 1_{i}\right),\left(\right.\right.\) rshORnot \(\left.\left._{i}\right)\right) ;\)
    \(\left(\right.\) rshORnot \(\left._{i}\right) \leftarrow\left(-r s h O\right.\) nnot \(\left._{i}\right) ;\)
    \(\left(m y 2_{i}\right) \leftarrow \operatorname{SecAnd}\left(\left(m y 2_{i}\right),\left(\right.\right.\) rshORnot \(\left.\left._{i}\right)\right) ;\)
    \(\left(e 2_{i}\right) \leftarrow \operatorname{SecAnd}\left(\left(e 2_{i}\right),\left(\right.\right.\) rshORnot \(\left.\left._{i}\right)\right) ;\)
    \(\left(m y_{i}\right) \leftarrow \operatorname{SecOr}\left(\left(m y 1_{i}\right),\left(m y 2_{i}\right)\right) ; \quad / /\) choice of \(m y\)
    \(\left(m y_{i}\right) \leftarrow \operatorname{SecAdd}\left(\left(m y_{i}\right),\left(R n d_{i}\right)\right) ;\)
    \(\left(e 1_{i}\right) \leftarrow \operatorname{Sec} \operatorname{Or}\left(\left(e 1_{i}\right),\left(e 2_{i}\right)\right) ; \quad / /\) choice of ey
    \(\left(e 1_{i}\right) \leftarrow \mathrm{B} 2 \mathrm{~A}\left(\left(e 1_{i}\right)\right)\);
    \(\left(e y_{i}\right) \leftarrow\left(e y_{i}+e 1_{i}\right) ;\)
    \(\left(e y_{i}\right) \leftarrow\left(e y_{i}+c d_{i}\right) ;\)
    return \(\left(\left(m y_{i}\right),\left(e y_{i}\right),\left(R n d_{i}\right)\right)\);
```

RemoveDecimal ${ }_{\text {round }}$ provides us an algorithm able to remove decimals and round up or down if necessary. It's important to compare $c x$ to 53 instead of 52 . The reason is that we first subtract to ey 1022 instead of 1023 when we were checking if the result was 0 in Algorithm 5.

Remark 3. Line 12-25 can be replace by using the last bit of removed values, after checking if a rounding must be done (check if $r s h O r N o t=1 \cdots 1$ ).

```
Algorithm 16: SetExponentZero \({ }_{\text {round }}\left(\left(e y_{i}\right),\left(s y_{i}\right),\left(b_{i}\right),\left(R n d_{i}\right)\right)\)
    Data: 16-bit arithmetic shares \(\left(e y_{i}\right)_{1 \leq i \leq n}\) for exponent value ey;
    1-bit boolean shares \(\left(s y_{i}\right)_{1 \leq i \leq n}\) for sign value sy
    64 -bit boolean shares \(\left(b_{i}\right)_{1 \leq i \leq n}\).
    Result: 16-bit boolean shares \(\left(e y_{i}\right)_{1 \leq i \leq n}\) for exponent value
                    \(e y+(52-c x) ;\)
    1-bit boolean shares \(\left(s y_{i}\right)_{1 \leq i \leq n}\) for sign value.
    \(\left(e y_{i}\right) \leftarrow \mathrm{A} 2 \mathrm{~B}\left(\left(e y_{i}\right)\right) ;\)
    \(\left(b_{i}^{\prime}\right) \leftarrow\left(R n d_{i}\right) ;\)
    \(\left(b_{i}^{\prime}\right) \leftarrow \operatorname{Sec} \operatorname{Or}\left(\left(b_{i}^{\prime}\right),\left(b_{i}\right)\right) ;\)
    \(\left(e y_{i}\right) \leftarrow \operatorname{SecAnd}\left(\left(e y_{i}, b_{i}^{\prime}\right)\right) ;\)
    \(\left(s y_{i}\right) \leftarrow \operatorname{SecAnd}\left(\left(s y_{i}, b_{i}^{\prime}\right)\right) ;\)
    return \(\left(\left(e y_{i}\right),\left(s y_{i}\right)\right)\);
```


## B. 3 Performances

Table 5. Time in microseconds

| Algorithm | 2 Shares | 3 Shares |
| :--- | :---: | ---: |
| SecFprAdd [6] | 7.533467 | 13.552070 |
| SecFprMul [6] | 5.563748 | 11.622864 |
| SecFprBaseInt ${ }_{\text {floor }}$ | 7.084196 | 13.284748 |
| SecFprBaseInt $_{\text {truncate }}$ | 5.502315 | 11.367418 |
| SecFprBaseInt $_{\text {rounding }}$ | 6.960874 | 13.404681 |
| SecFprUrsh $_{\text {trunc }}$ | 0.095197 | 0.172693 |
| SecFprUrsh $_{\text {floor }}$ | 0.113149 | 0.219650 |


[^0]:    ${ }^{4}$ Zerofloor $^{5}=$ Zero $_{\text {trunc }}=1023$ and Zeroround $_{\text {round }}=1022$
    ${ }^{5}$ Pseudo-code in Appendix: SecFprExtract - Algorithm 12

[^1]:    ${ }^{6}$ First case is not affected by RemoveDecimal as mantissa is set to 0 .

[^2]:    ${ }^{7}$ The pseudo-code is provided in Appendix B

